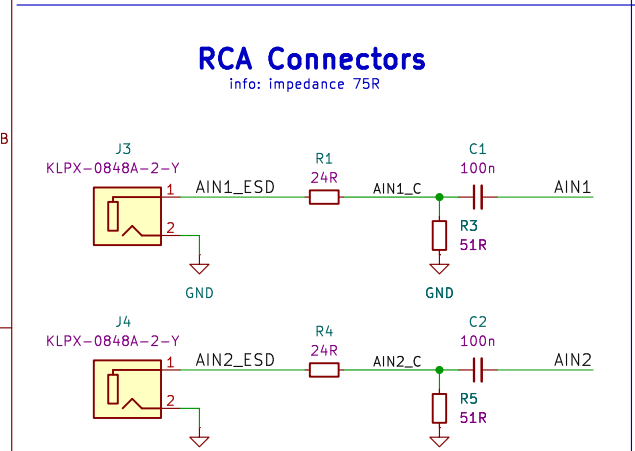
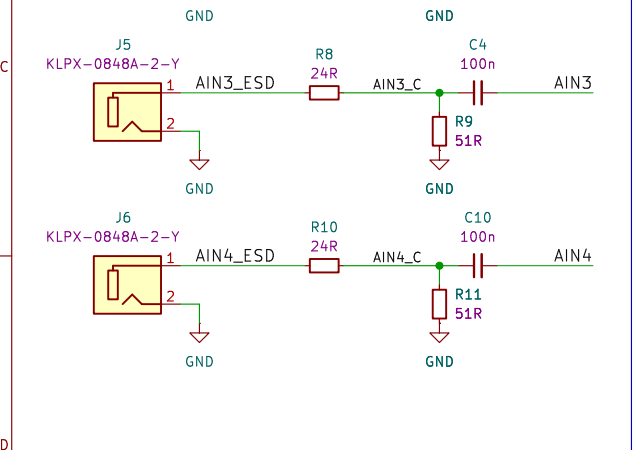


Debug Power Supply Connector

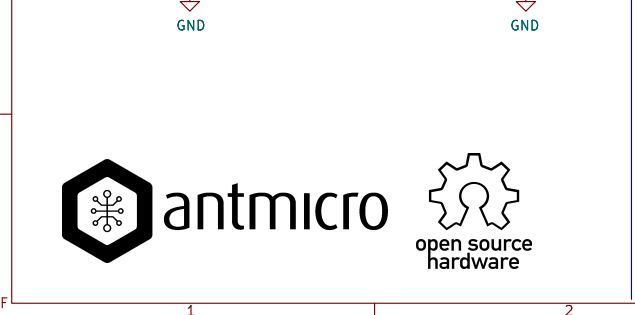


info: impedance 75R



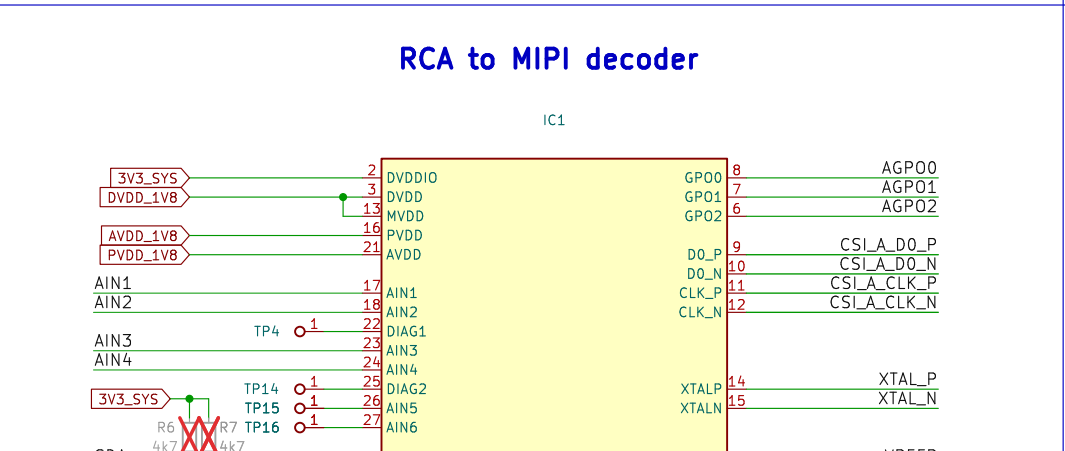
RCA ESD

The diagram illustrates the ESD protection circuit for the RCA module. It shows four input pins: AIN1_ESD, AIN2_ESD, AIN3_ESD, and AIN4_ESD. Each pin is connected to a green line representing the signal path. AIN1_ESD and AIN2_ESD are connected to a red diode (D1 and D2 respectively) pointing towards the signal path, with the other terminal connected to GND. AIN3_ESD and AIN4_ESD are connected to a red diode (D3 and D4 respectively) pointing away from the signal path, with the other terminal connected to GND.



Power Supply

The power supply circuit for the TLV70018BCKR (IC2) is shown. The input is 3V3_SYS, which is filtered by capacitors C31 (10uF), C7 (100nF), and C8 (1uF) to ground. The input is connected to the VIN pin (pin 1) of IC2. The EN pin (pin 3) is connected to the output of the filter. The VOUT pin (pin 5) is connected to a TP3 test point and a resistor R30 (0R). The output of R30 is connected to a network of capacitors C9 (1uF) and C32 (10uF) to ground. The output then splits into three parallel branches, each with a 220Z/1.5A capacitor to ground and a PWR_FLAG signal line. The signal lines are connected to 1V8_SYS, DVDD_1V8, and AVDD_1V8. The PVDD_1V8 signal line is also present but not connected to a specific output.



SDA 30
SCL 31
RESET 28
ALSB 29
PWRDWN 32

SDATA
SCLK
RESET
ALSB
PWRDWN

L => I2C Addr 0x40
H => I2C Addr 0x42

VREFP 19
VREFN 20
INTRQ 5

EP 33
DGND 1

GND

3V3_SYS

R2 1k

ALSB

1 2

J7 61300211121

GND

VREFP

C6 100n

VREFN

XTAL_P

R16 1M

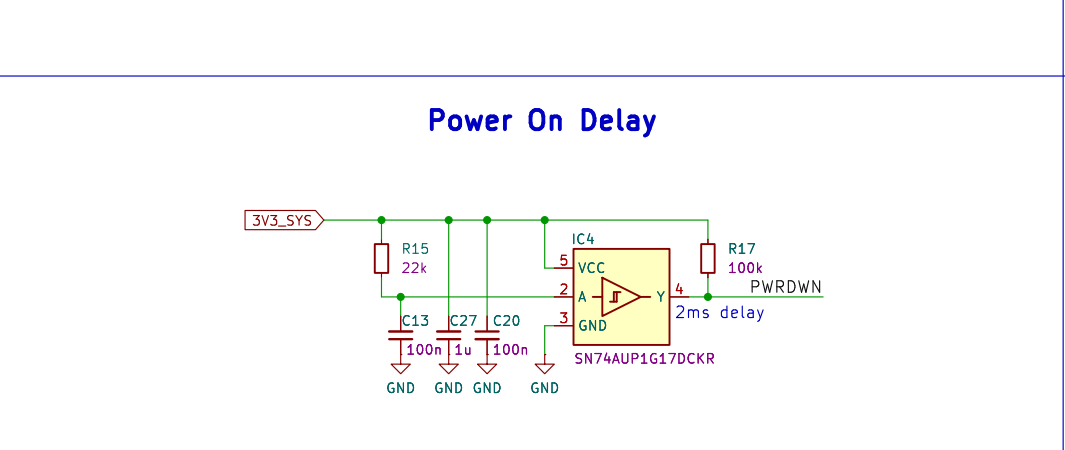
XTAL_N

Y1 28.63636 MHz

C22 10p

C23 10p

GND GND



Test points

RESET 1 ○ TP8

PWRDWN 1 ○ TP9

INTRQ 1 ○ TP2

3V3_SYS 1 ○ TP5

1 ○ TP13

SDA 1 ○ TP6

SCL 1 ○ TP7

AGPO2 1 ○ TP1

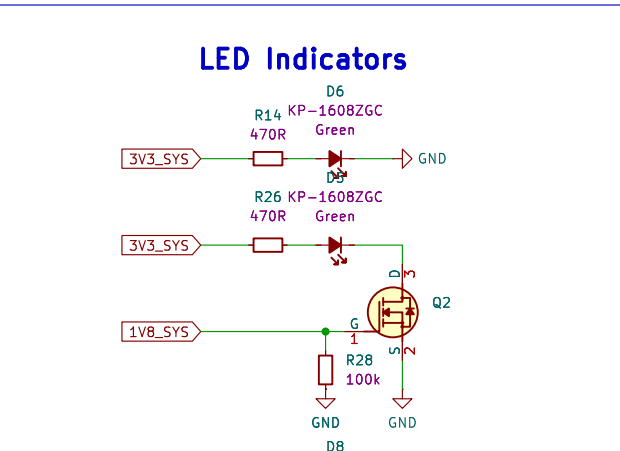
AGPO1 1 ○ TP10

AGPO0 1 ○ TP11

ALSB 1 ○ TP12

↓

GND



AGP00

3V3_SYS

R12 KP-1608ZGC
470R Green

R29 100k

GND

Q3

AGP01

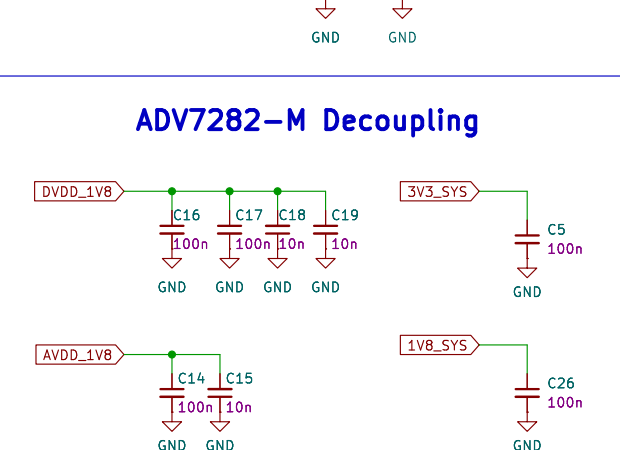
3V3_SYS

R13 KP-1608ZGC
470R Green

R31 100k

GND

Q4



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 Sheet: /
 File: cvbs-mipi
Title: CVBS
 Size: A3
 KiCad E.D.A. 9



50-pin FFC Connector

