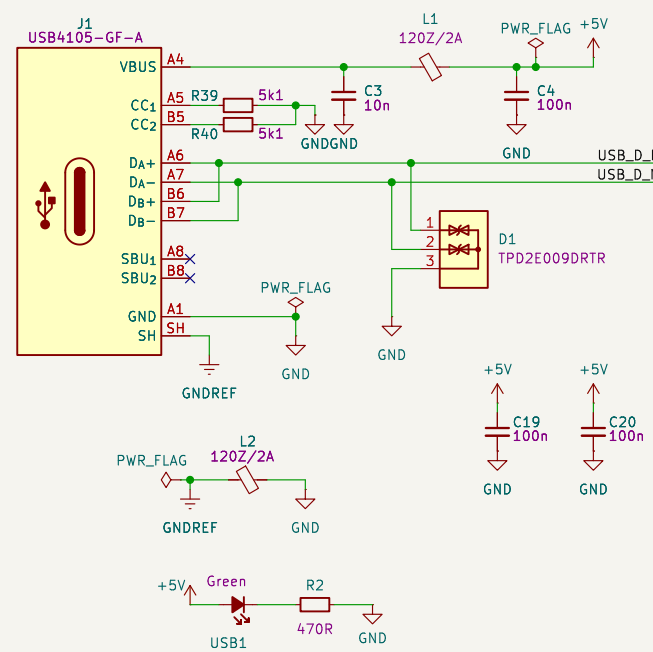
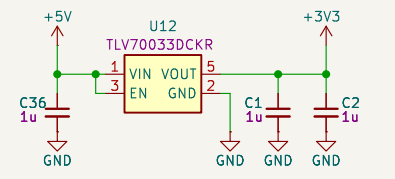


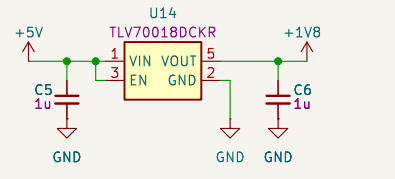
## USB-C



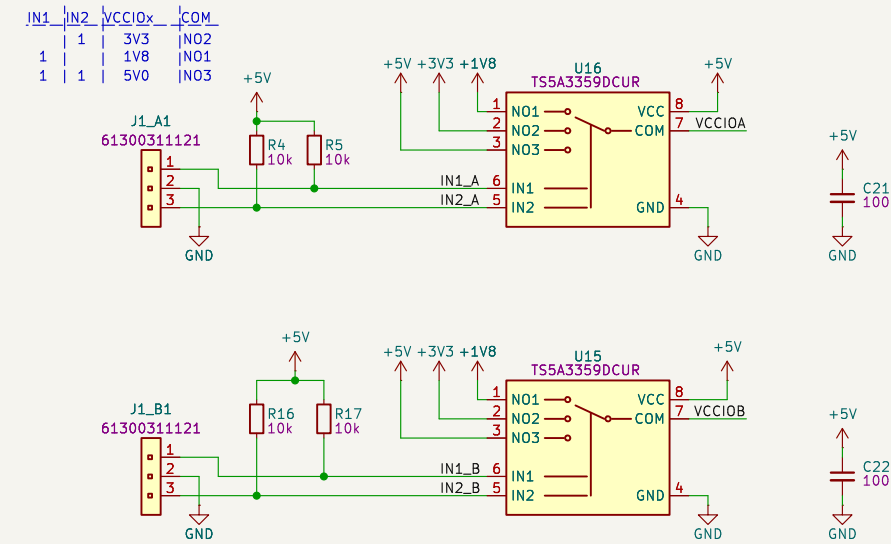
## 5V -> 3.3V LDO 400mA



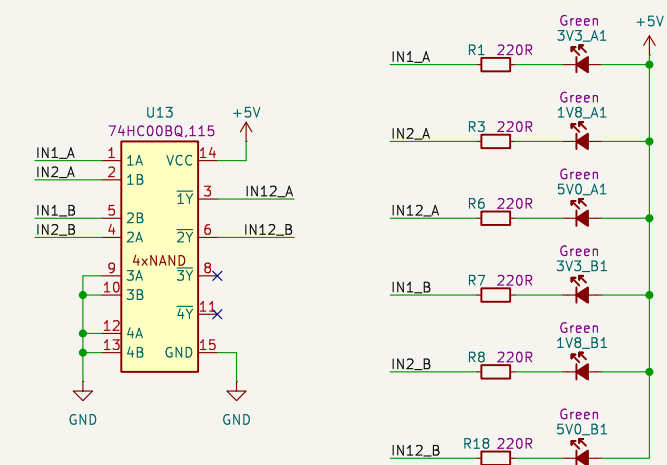
## 3V3 -> 1.8V LDO 200mA



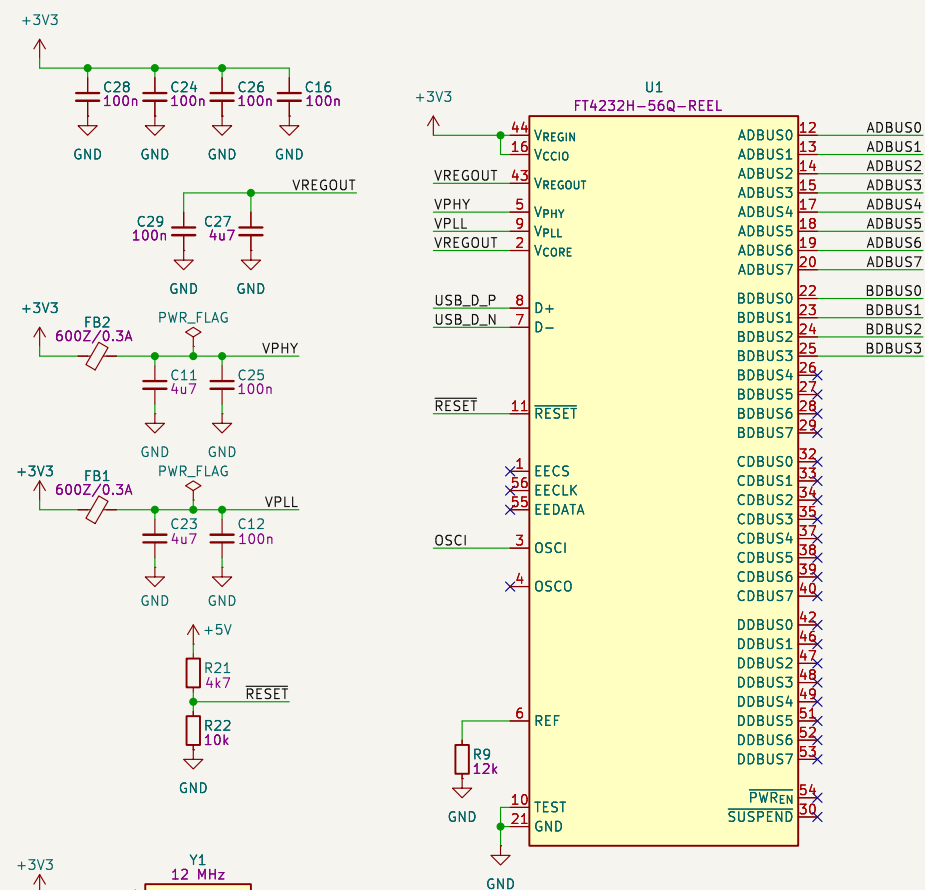
## Voltage selection



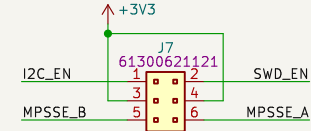
## Voltage indicator



## JTAG/UART to USB



## Mode selector



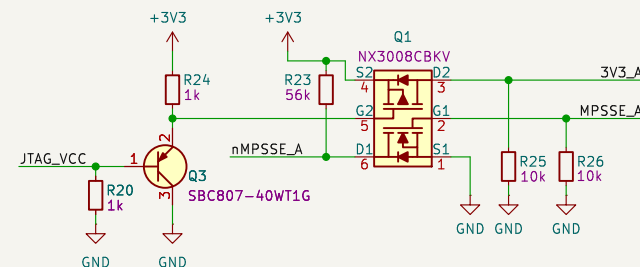
## Pin function table

| PIN    | UART | JTAG     | SPI      | I2C        | SWD      |
|--------|------|----------|----------|------------|----------|
| ADBUS0 | TXD  | JTAG_TCK | SPL_SCK  |            | SW_CLK   |
| ADBUS1 | RXD  | JTAG_TDI | SPL_MOSI |            | SW_IO(o) |
| ADBUS2 | RTS  | JTAG_TDO | SPL_MISO |            | SW_IO(i) |
| ADBUS3 | CTS  | JTAG_TMS | SPL_CS   |            |          |
| BDBUS0 | TXD  | JTAG_TCK | SPL_SCK  | I2C_SCL    |          |
| BDBUS1 | RXD  | JTAG_TDI | SPL_MOSI | I2C_SDA(o) |          |
| BDBUS2 | RTS  | JTAG_TDO | SPL_MISO | I2C_SDA(i) |          |
| BDBUS3 | CTS  | JTAG_TMS | SPL_CS   |            |          |

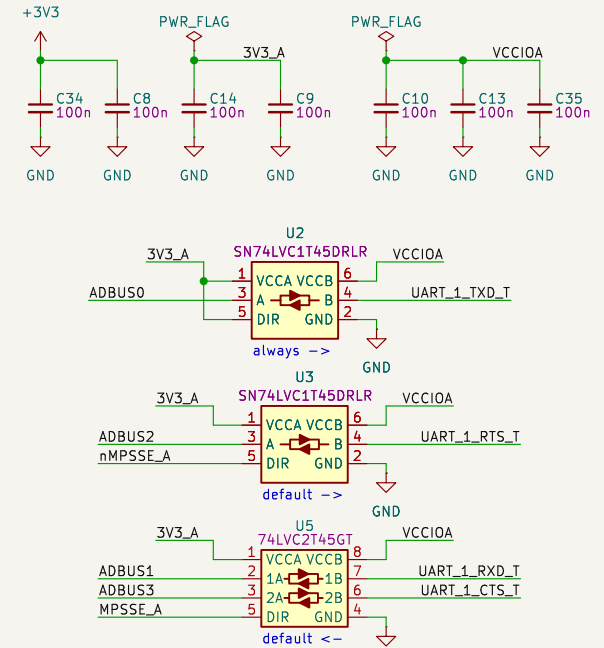
MPSSE mode = JTAG/SPI, connector selected by JTAG\_VCC

## Interface A

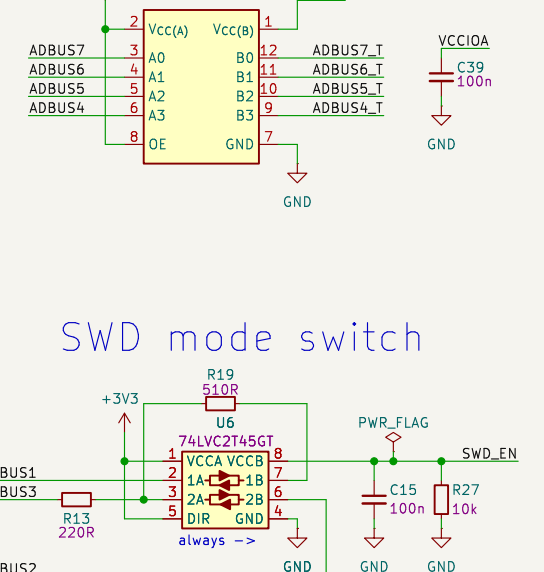
buffer enable and direction



voltage level translation

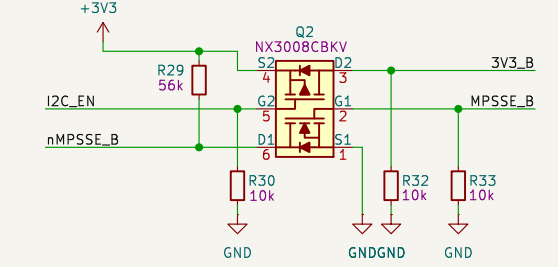


## SWD mode switch

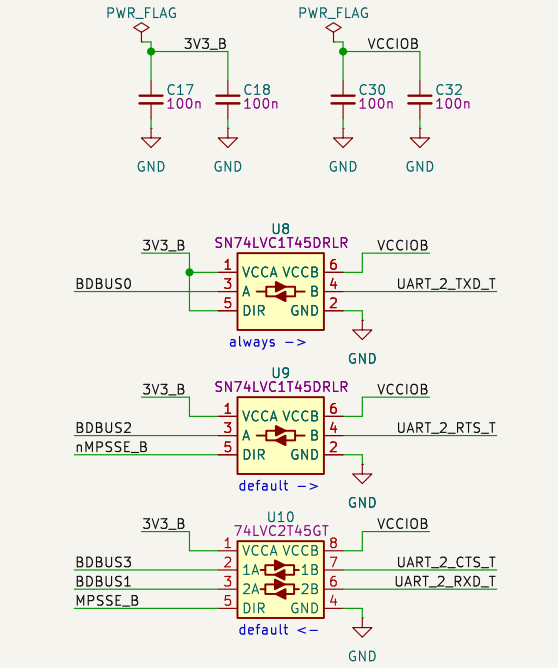


## Interface B

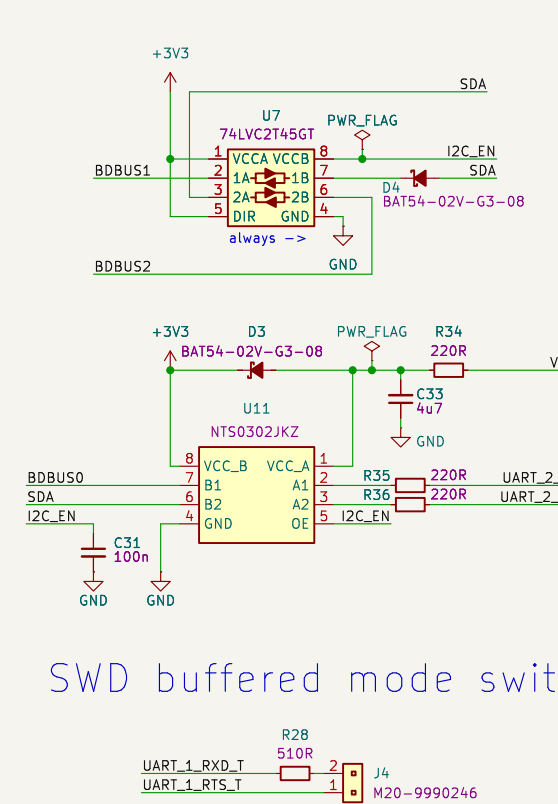
buffer enable and direction



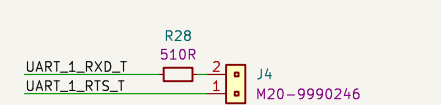
voltage level translation



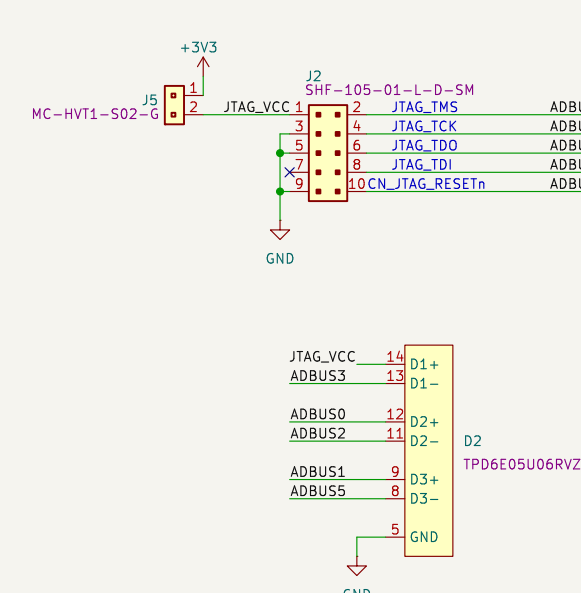
I2C buffer w/ SDO+SDI switch



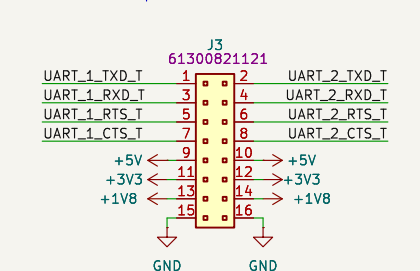
SWD buffered mode switch



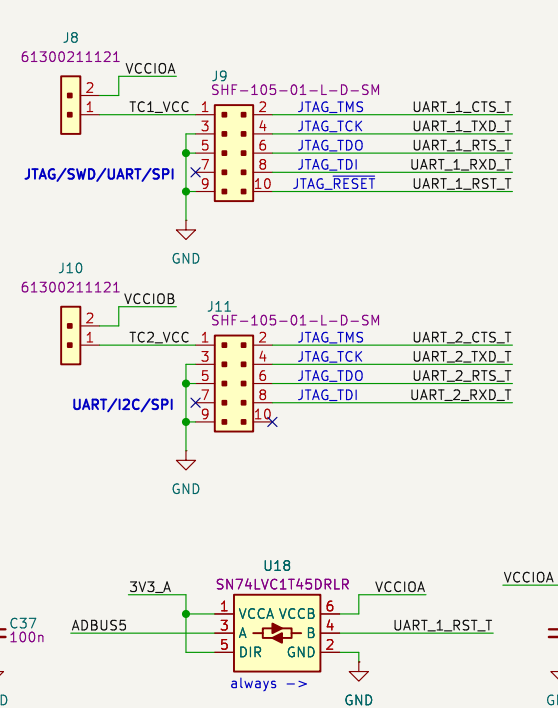
## JTAG connector



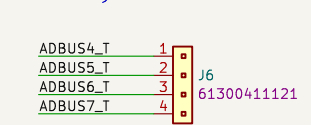
## Goldpin connector



## TagConnect connectors



## Auxillary connector



## Line LED'S

