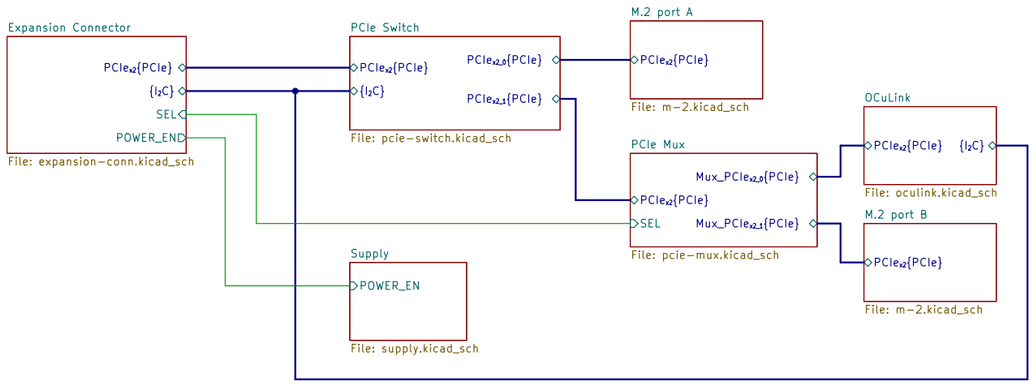
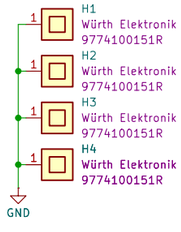
A1
Mech_M2_2280_H2.5mm

A2
Mech_M2_2242_H2.5mm



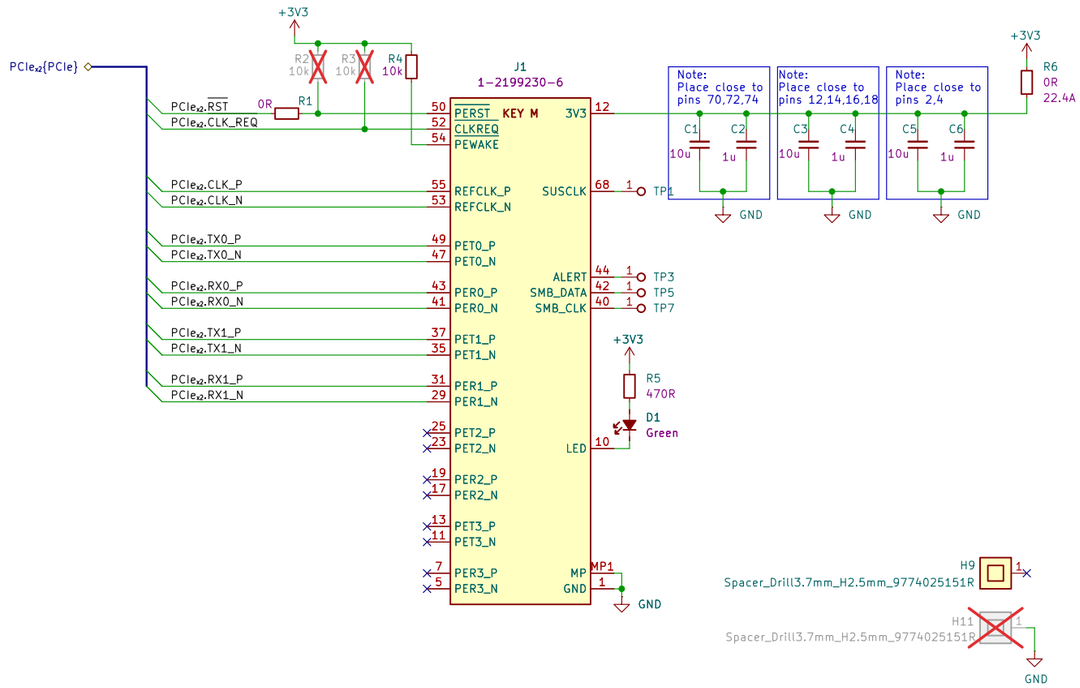


Commit: f3082c87
Antmicro
Sheet: /
File: antmicro-jetson-orin-baseboard-pcie-switch-expansion-hw.kicad_sch

Title: antmicro-jetson-orin-baseboard-pcie-switch-expansion-hw

Size: A3	Date: 2026-08-26	**Rev: 1.0.0**
KiCad E.D.A. 10.0.6		Id: 1/8

M.2 Slot



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Commit: f3082c87

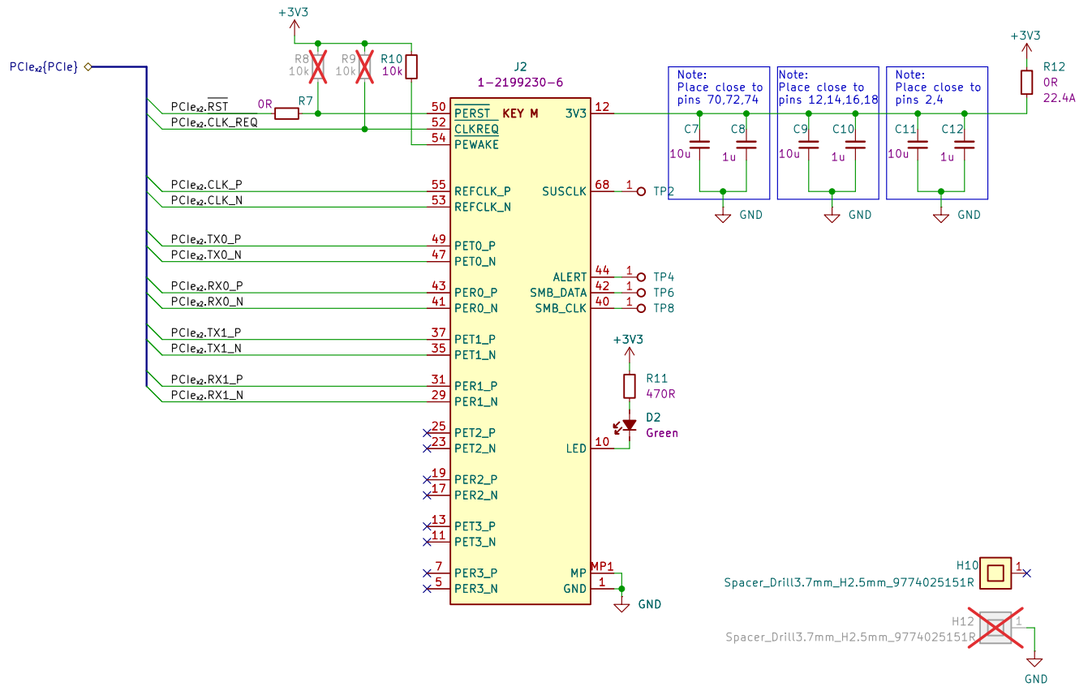
Antmicro

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File: m-2.kicad_sch

Title: antmicro-jetson-orin-baseboard-pcie-switch-expansion-hw

Size: A3	Date: 2026-08-26	**Rev: 1.0.0**
KiCad E.D.A. 10.0.6		Id: 2/8

M.2 Slot



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Commit: f3082c87

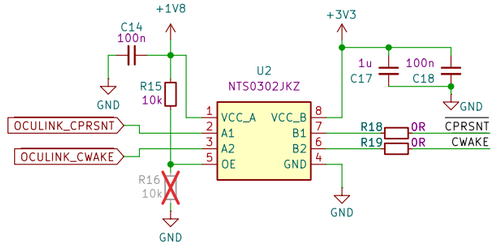
Antmicro

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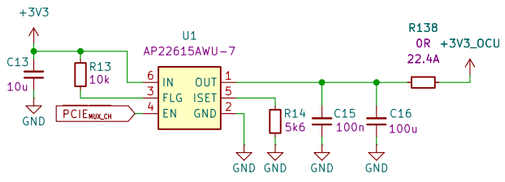
Title: antmicro-jetson-orin-baseboard-pcie-switch-expansion-hw

Size: A3	Date: 2026-08-26	**Rev: 1.0.0**
KiCad E.D.A. 10.0.6		Id: 3/8

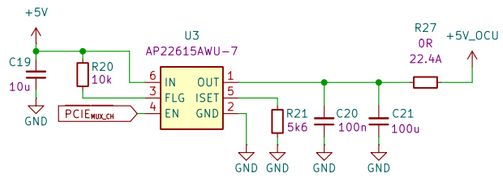
1V8 to 3V3 LLC



OCuLink e-fuses

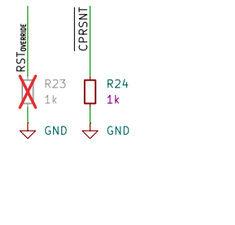


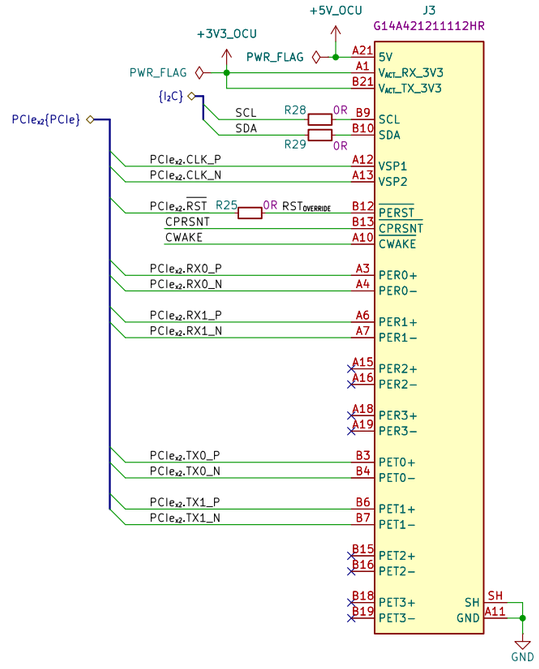
ISET resistor value:
target current limit = 1.2 A (2x peak OCuLink pin current)
AP22615 datasheet, page 3, ISET pin description
I_{LIM} (A) = 6800 / R_{LIM} (Ohms)
1.2 A = 6800 / R_{LIM} Ohms
R_{LIM} = 6800 / 1.2 = 5666 Ohms ~= 5.6 kOhms
real current limit ~= 1.214 A



ISET resistor value:
target current limit = 1.2 A (2x peak OCuLink pin current)
AP22615 datasheet, page 3, ISET pin description
I_{LIM} (A) = 6800 / R_{LIM} (Ohms)
1.2 A = 6800 / R_{LIM} Ohms
R_{LIM} = 6800 / 1.2 = 5666 Ohms ~= 5.6 kOhms
real current limit ~= 1.214 A

OCuLink Connector





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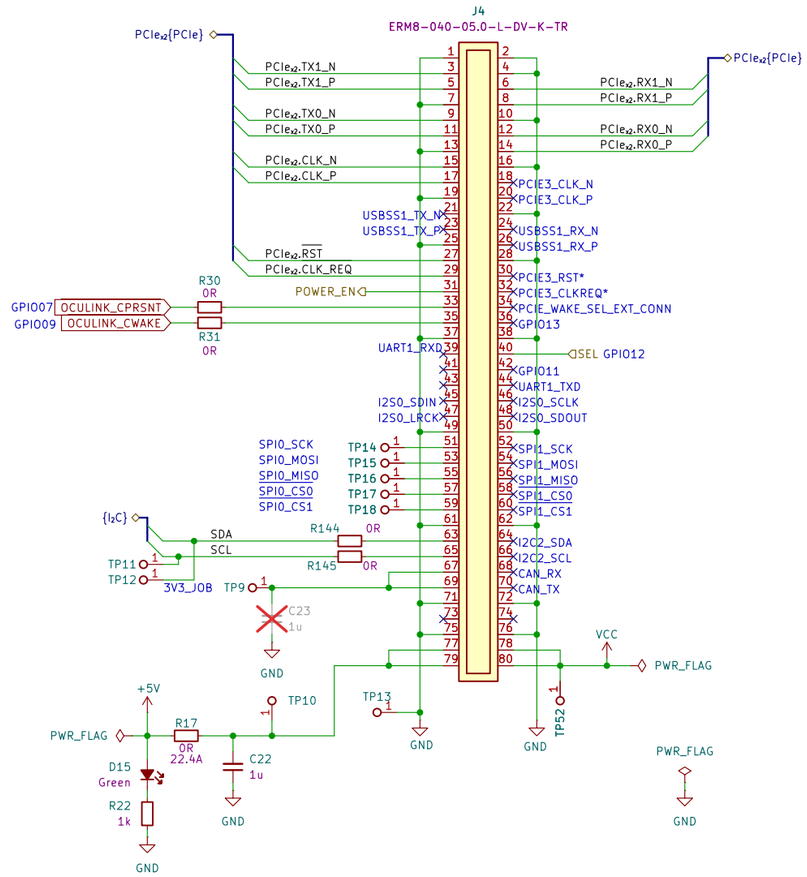
open source hardware

Commit: f3082c87
Antmicro
Sheet: /OCuLink/
File: oculink.kicad_sch

Title: antmicro-jetson-orin-baseboard-pcie-switch-expansion-hw

Size: A3	Date: 2026-08-26	**Rev: 1.0.0**
KiCad E.D.A. 10.0.6		Id: 4/8

Expansion connector



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Commit: f3082c87

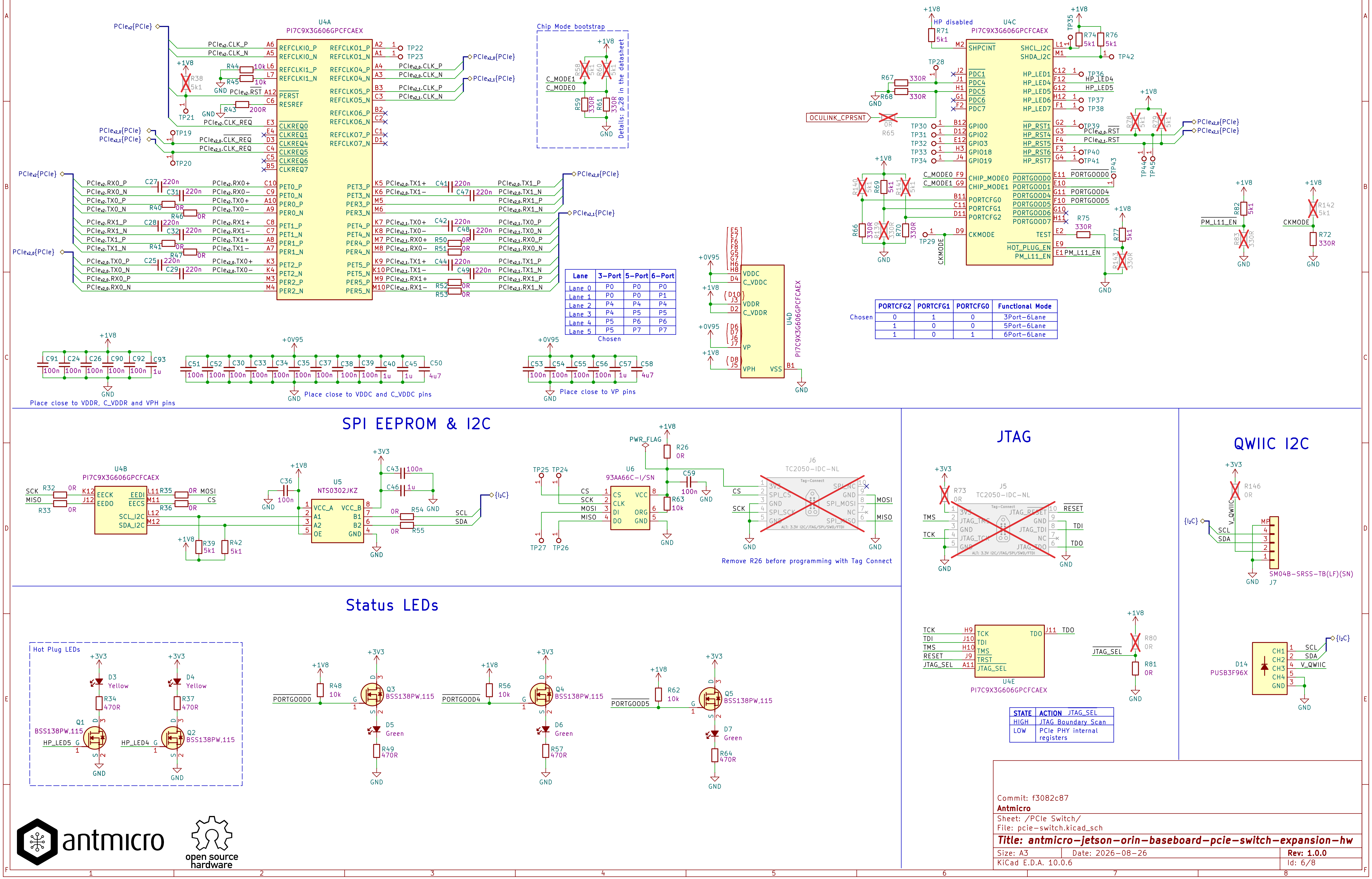
Antmicro

Sheet: /Expansion Connector/
File: expansion-conn.kicad_sch

Title: antmicro-jetson-orin-baseboard-pcie-switch-expansion-hw

Size: A3	Date: 2026-08-26	**Rev: 1.0.0**
KiCad E.D.A. 10.0.6		Id: 5/8

PCIe Switch



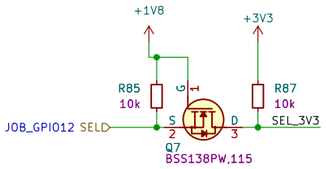
Lane	3-Port	5-Port	6-Port
Lane 0	P0	P0	P0
Lane 1	P0	P0	P1
Lane 2	P4	P4	P4
Lane 3	P4	P5	P5
Lane 4	P5	P6	P6
Lane 5	P5	P7	P7
	Chosen		

	PORTCFG2	PORTCFG1	PORTCFG0	Functional Mode
Chosen	0	1	0	3Port-6Lane
	1	0	0	5Port-6Lane
	1	0	1	6Port-6Lane

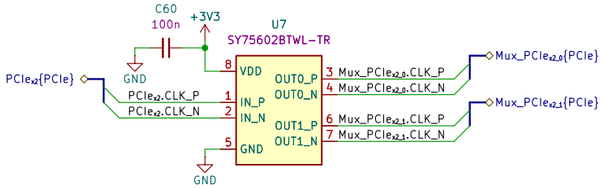
STATE	ACTION JTAG_SEL
HIGH	JTAG Boundary Scan
LOW	PCIe PHY internal registers

Commit: f3082c87
Antmicro
Sheet: /PCIe Switch/
File: pcie-switch.kicad_sch
Title: antmicro-jetson-orin-baseboard-pcie-switch-expansion-hw
Size: A3 | Date: 2026-08-26 | **Rev: 1.0.0**
KiCad E.D.A. 10.0.6 | Id: 6/8

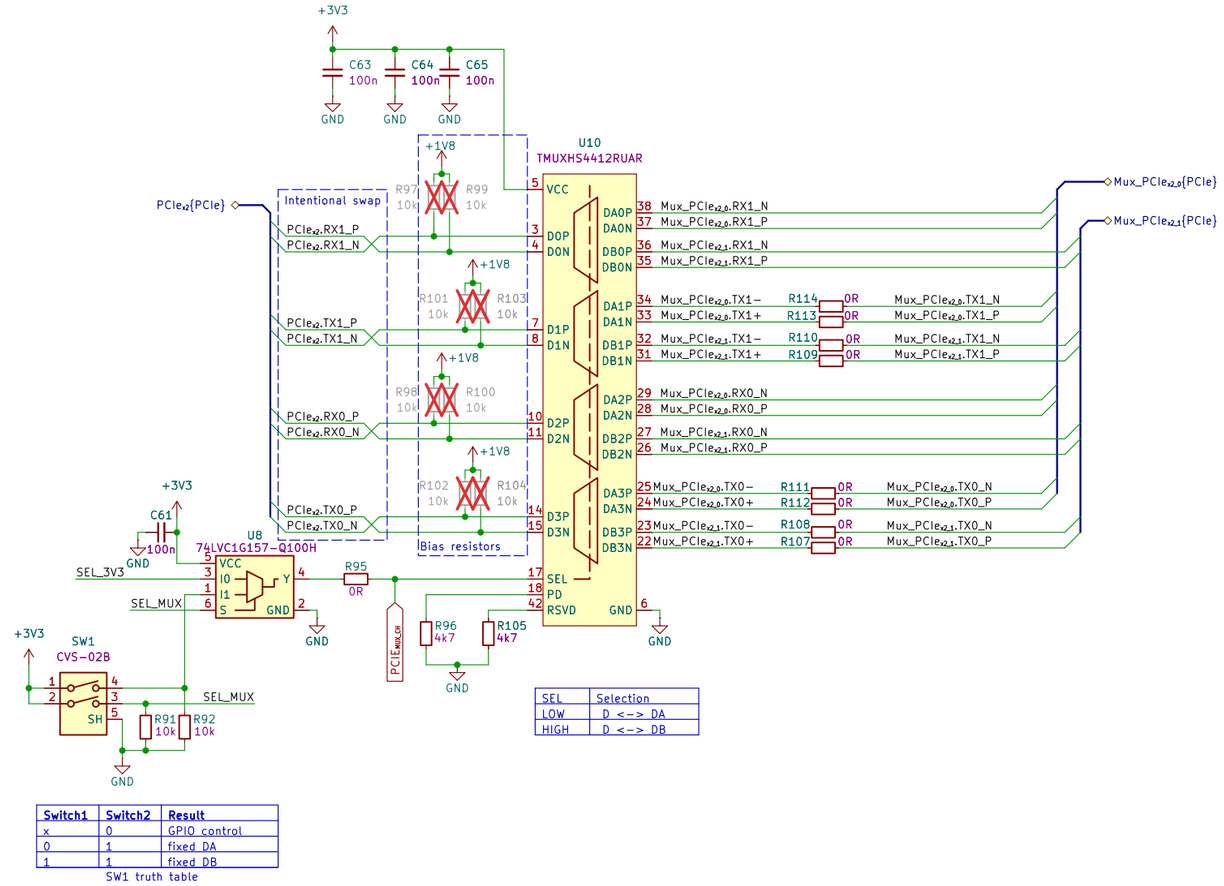
Level shifter



PCIe clock buffer



PCIe Multiplexer

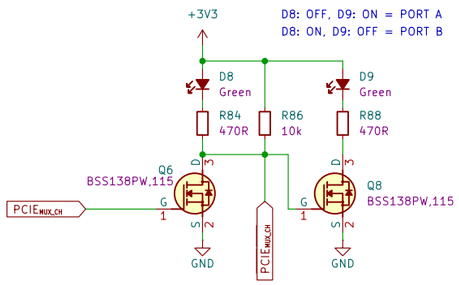


SEL	Selection
LOW	D <-> DA
HIGH	D <-> DB

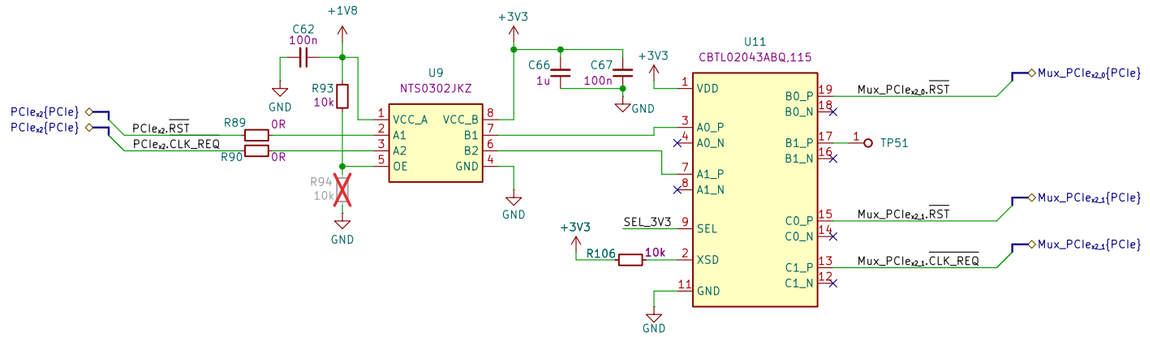
Switch1	Switch2	Result
x	0	GPIO control
0	1	fixed DA
1	1	fixed DB

SW1 truth table

Status LEDs



PCIe control signals



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Commit: f3082c87

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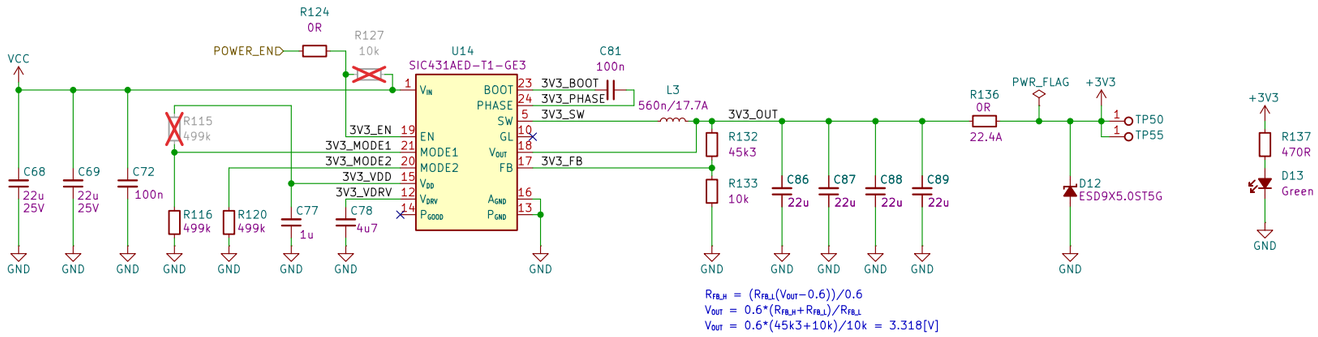
Sheet: /PCIe Mux/

File: pcie-mux.kicad_sch

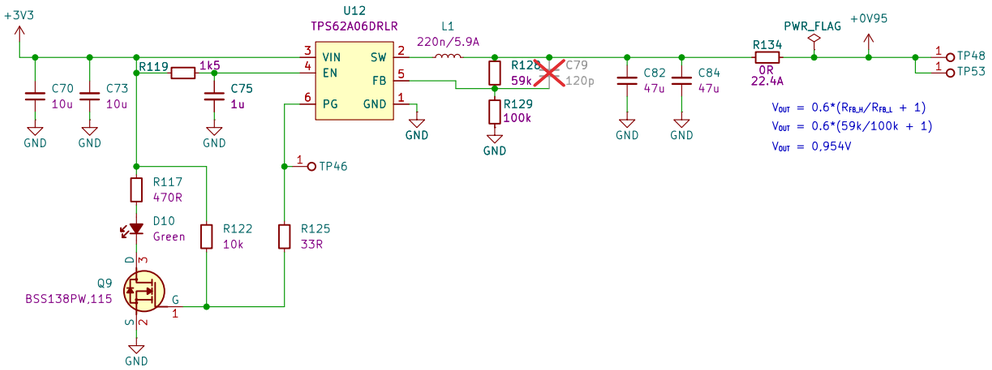
Title: antmicro-jetson-orin-baseboard-pcie-switch-expansion-hw

Size: A3	Date: 2026-08-26	**Rev: 1.0.0**
KiCad E.D.A. 10.0.6		Id: 7/8

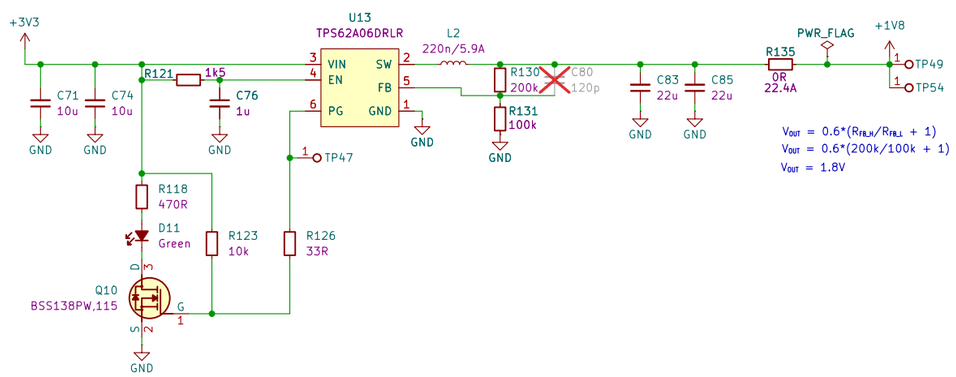
JOB VCC to 3V3 DCDC Converter



3V3 to 0V95 DCDC Converter



3V3 to 1V8 DCDC Converter



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Commit: f3082c87

Antmicro

Sheet: /Supply/
File: supply.kicad_sch

Title: antmicro−jetson−orin−baseboard−pcie−switch−expansion−hw

Size: A3	Date: 2026−08−26	**Rev: 1.0.0**
KiCad E.D.A. 10.0.6		Id: 8/8