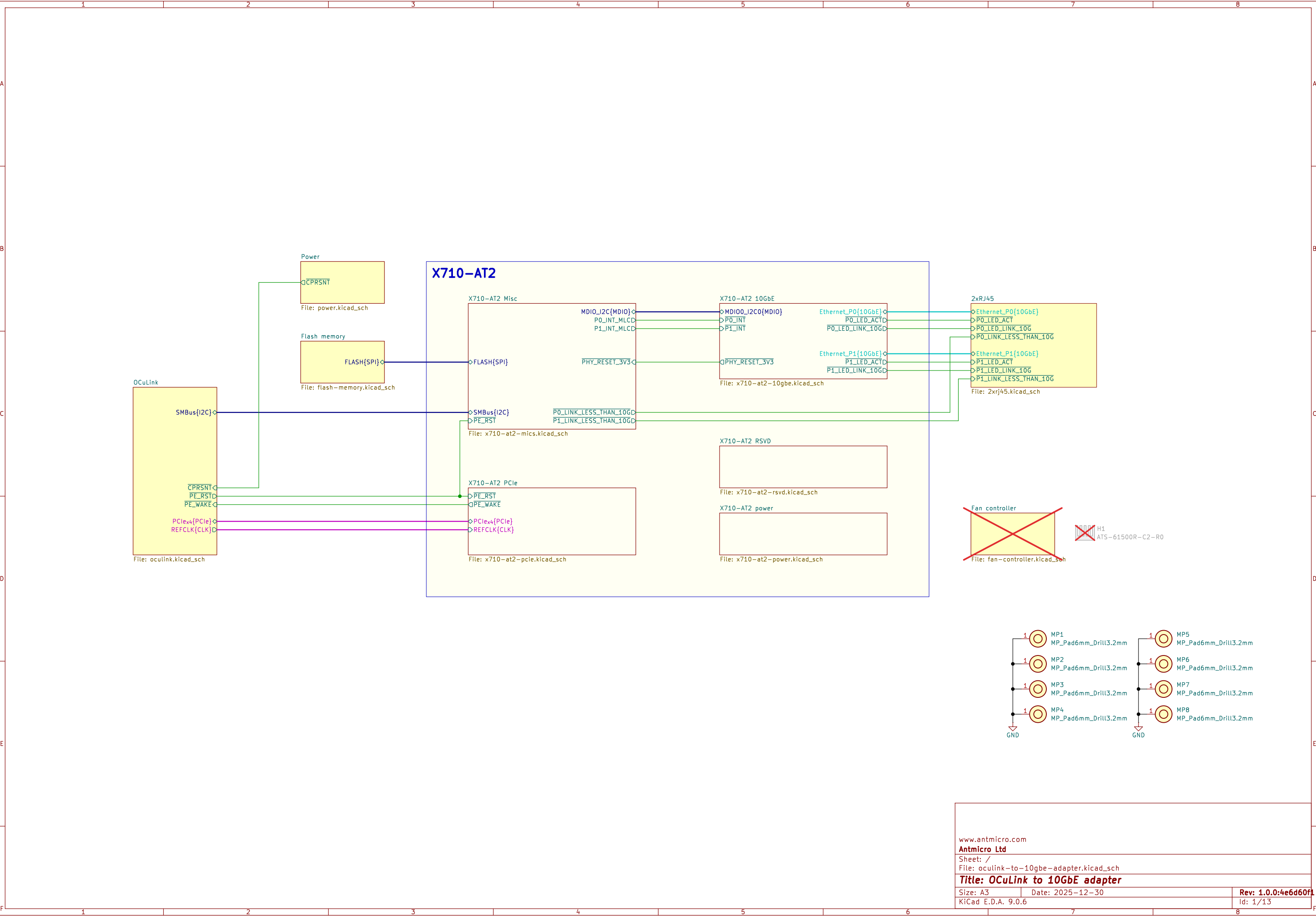
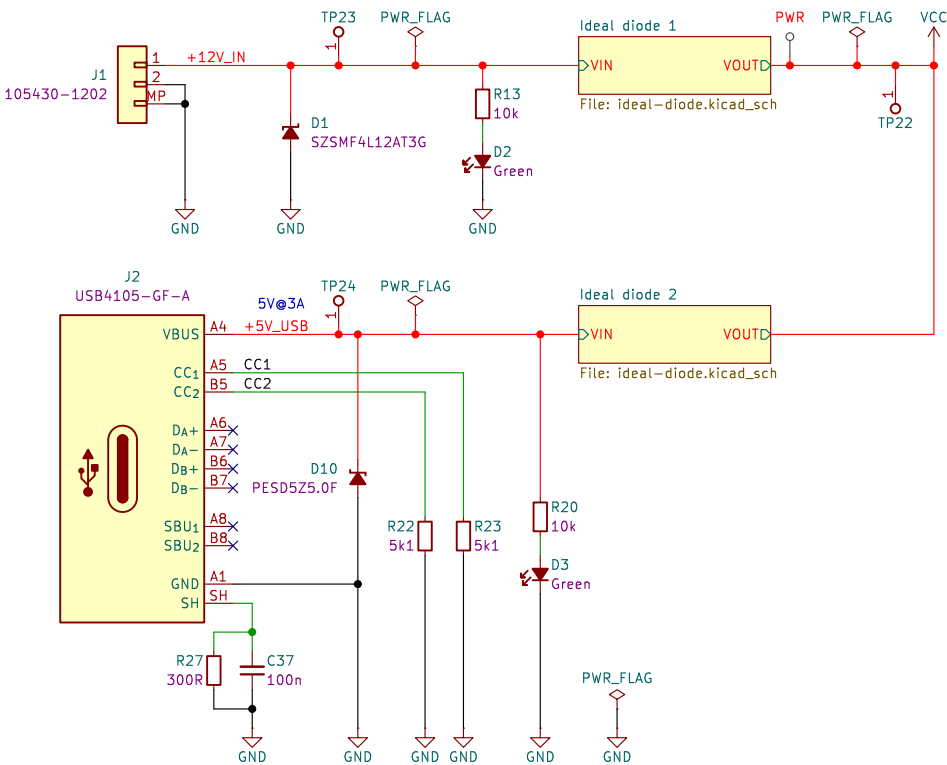




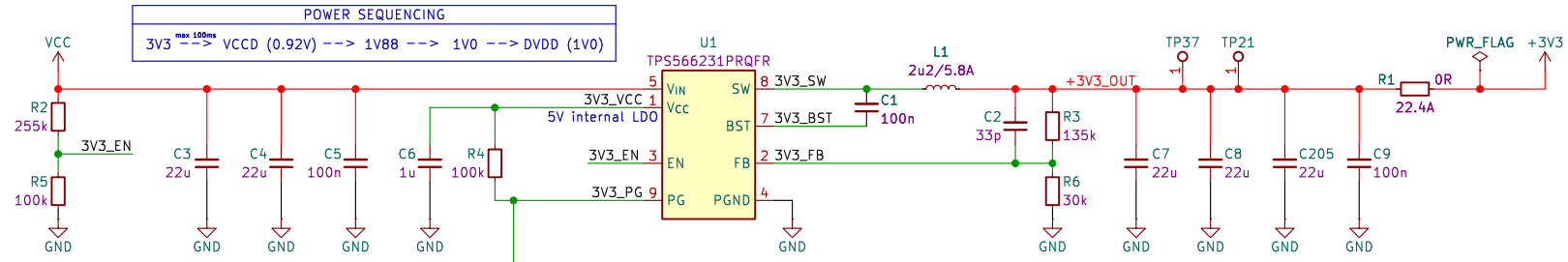
Power Inputs



3V3 DC-DC

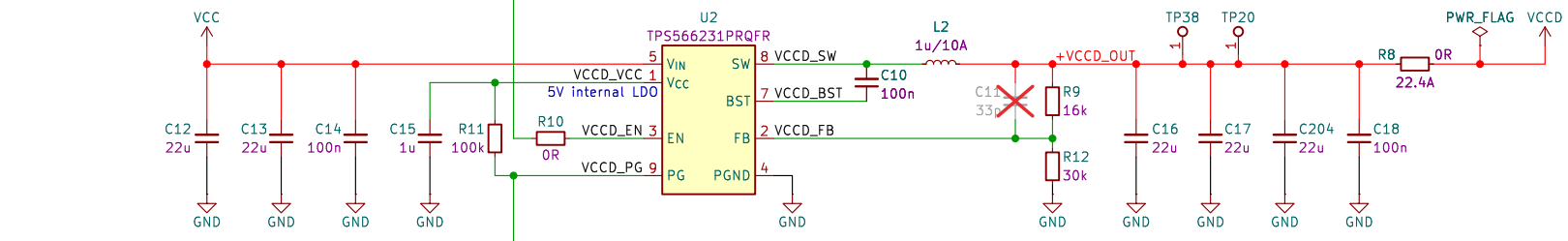
3.14-3.47V
Max ripple: 70mV_{p-p}

UVLO
3V3 rail is first in
the power sequence,
so deasserting its
enable pin
turns off remaining
DC-DC converters



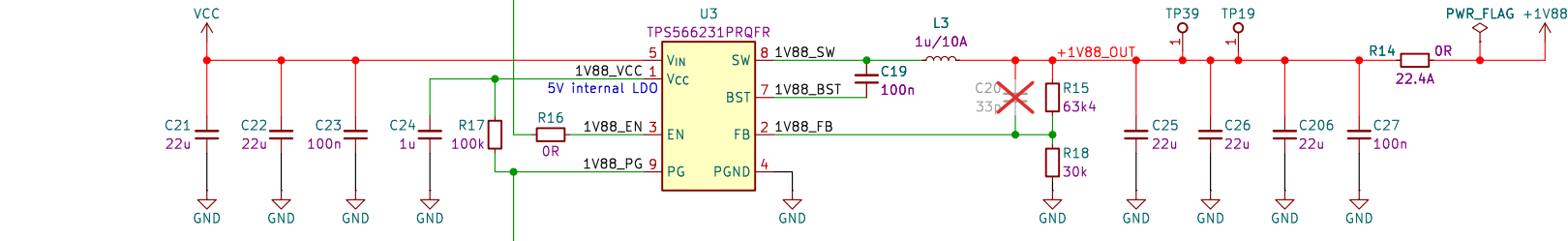
0V92 DC-DC

0.87-0.96V
Max ripple: 20mV_{p-p}



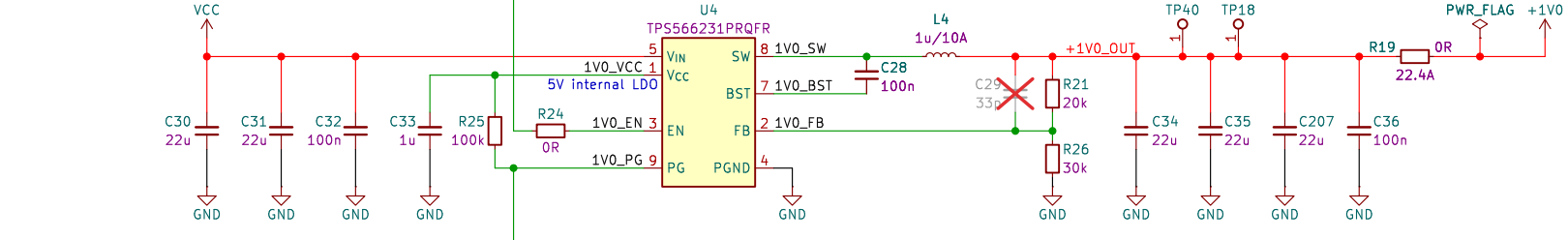
1V88 DC-DC

1.8236-1.9364V
Max ripple: 50mV_{p-p}



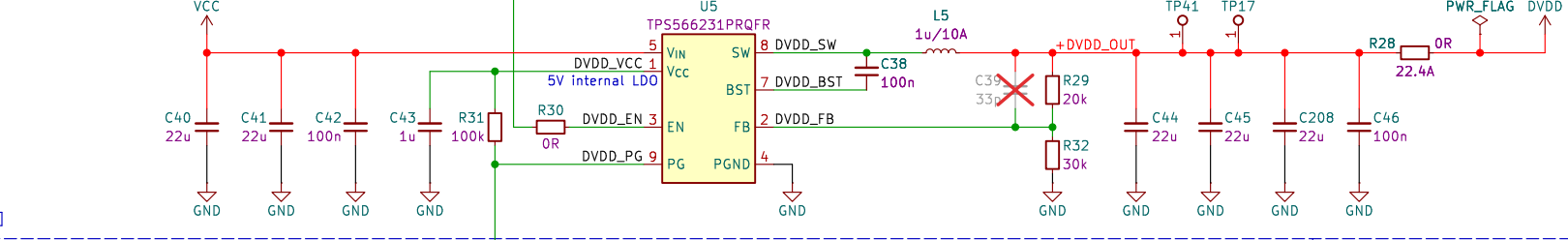
1V DC-DC

0.95-1.05V
Max ripple: 10mV_{p-p}

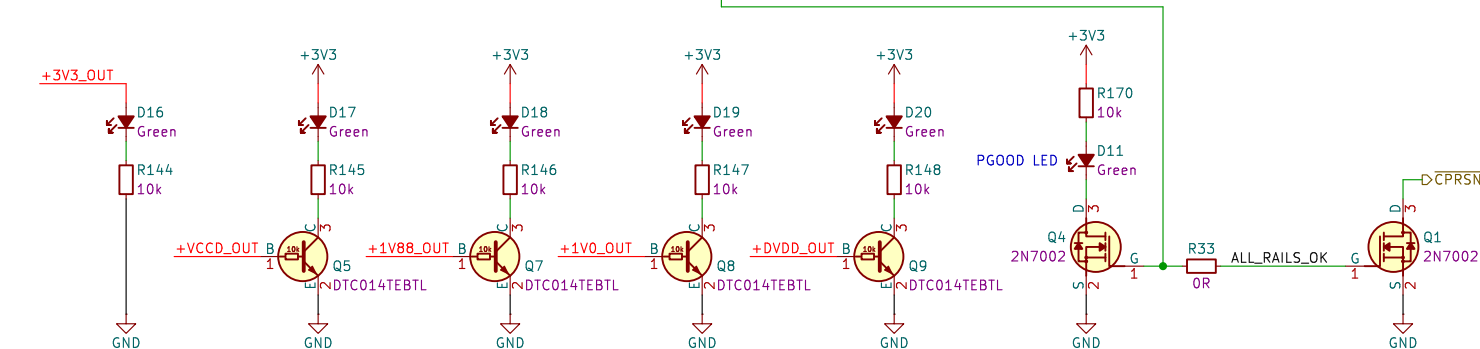


DVDD DC-DC

0.95-1.05V
Max ripple: 30mV_{p-p}

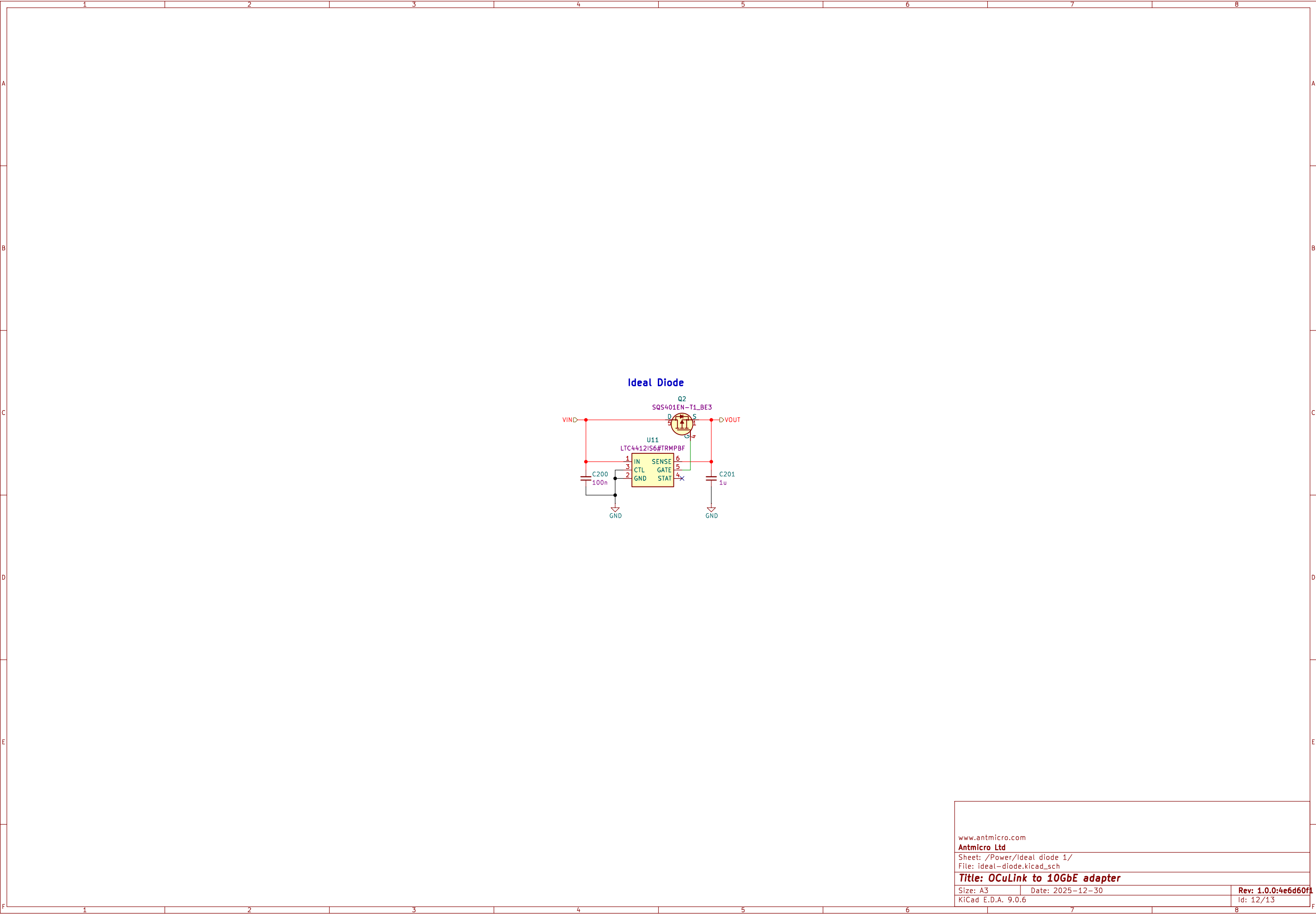


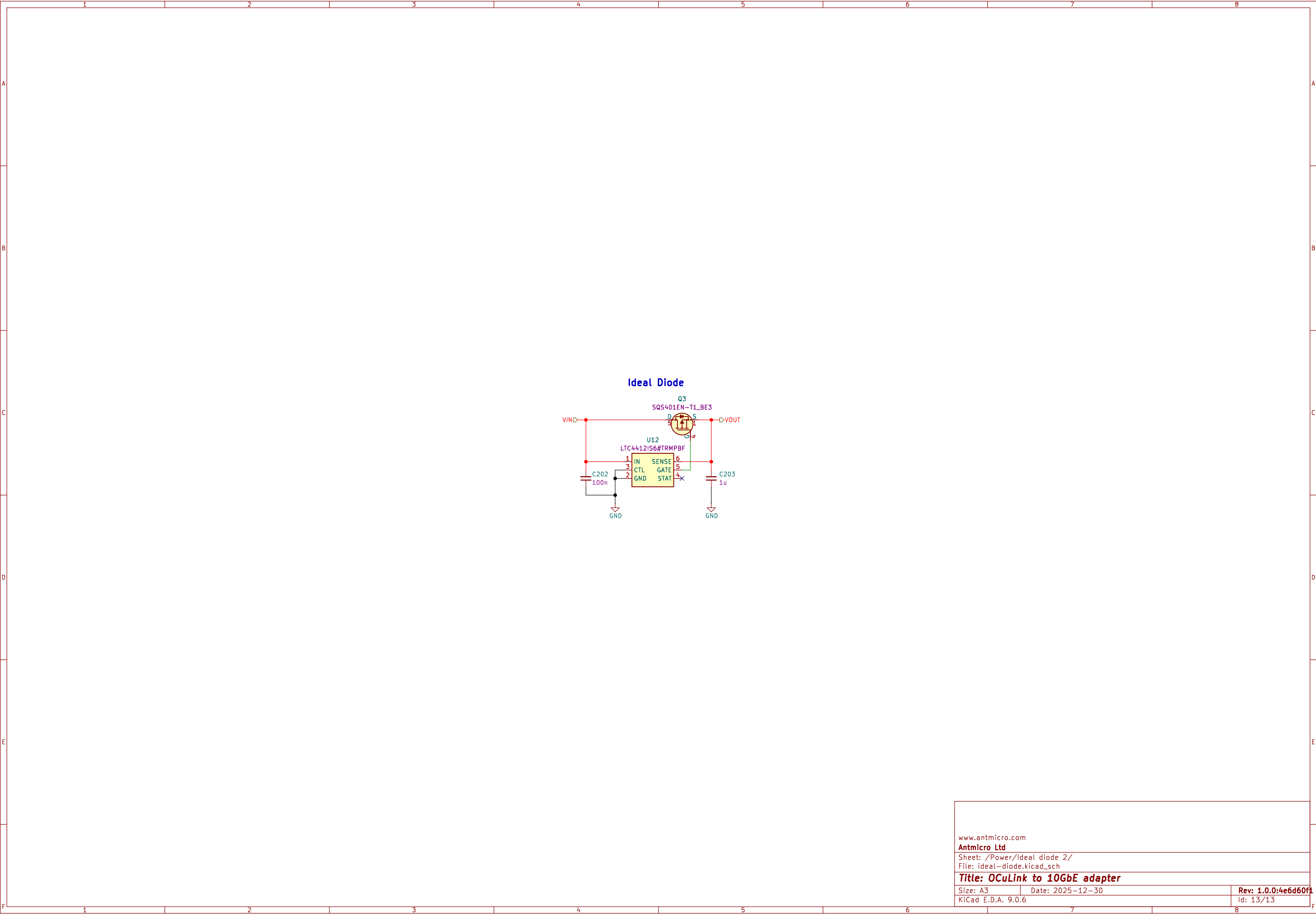
$$V_{out} = 0.6 \times (1 + R_H/R_L) [V]$$



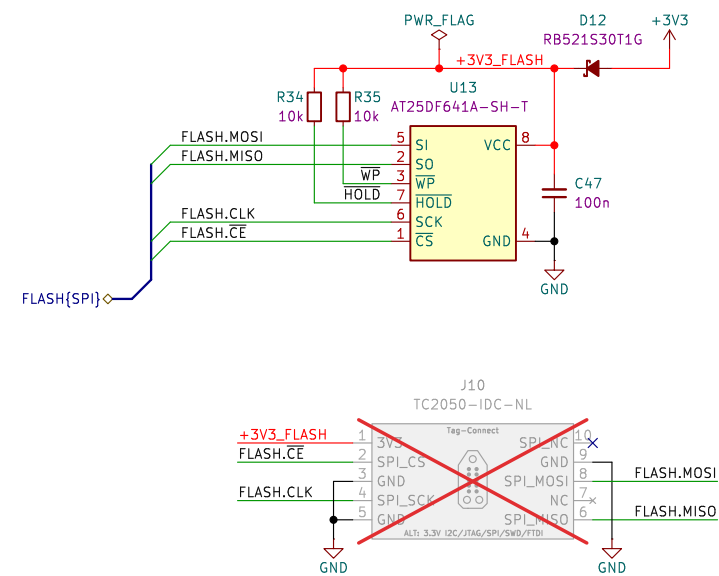
ICT Testpoints
[place on bottom]

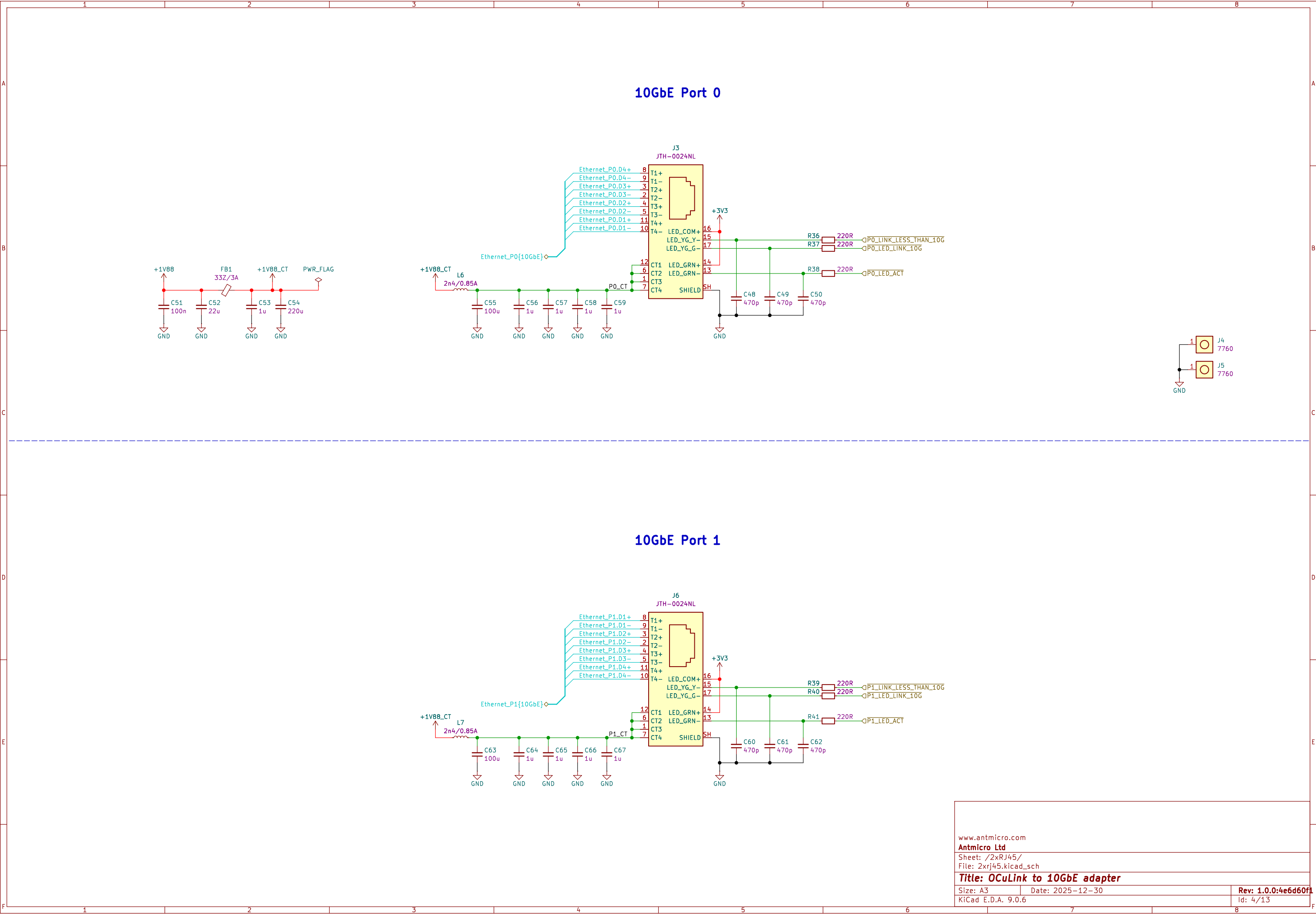
- +12V_IN 1 TP27
- +5V_USB 1 TP28
- VCC 1 TP29
- +3V3 1 TP30
- VCCD 1 TP31
- +1V88 1 TP32
- +1V0 1 TP33
- DVDD 1 TP34



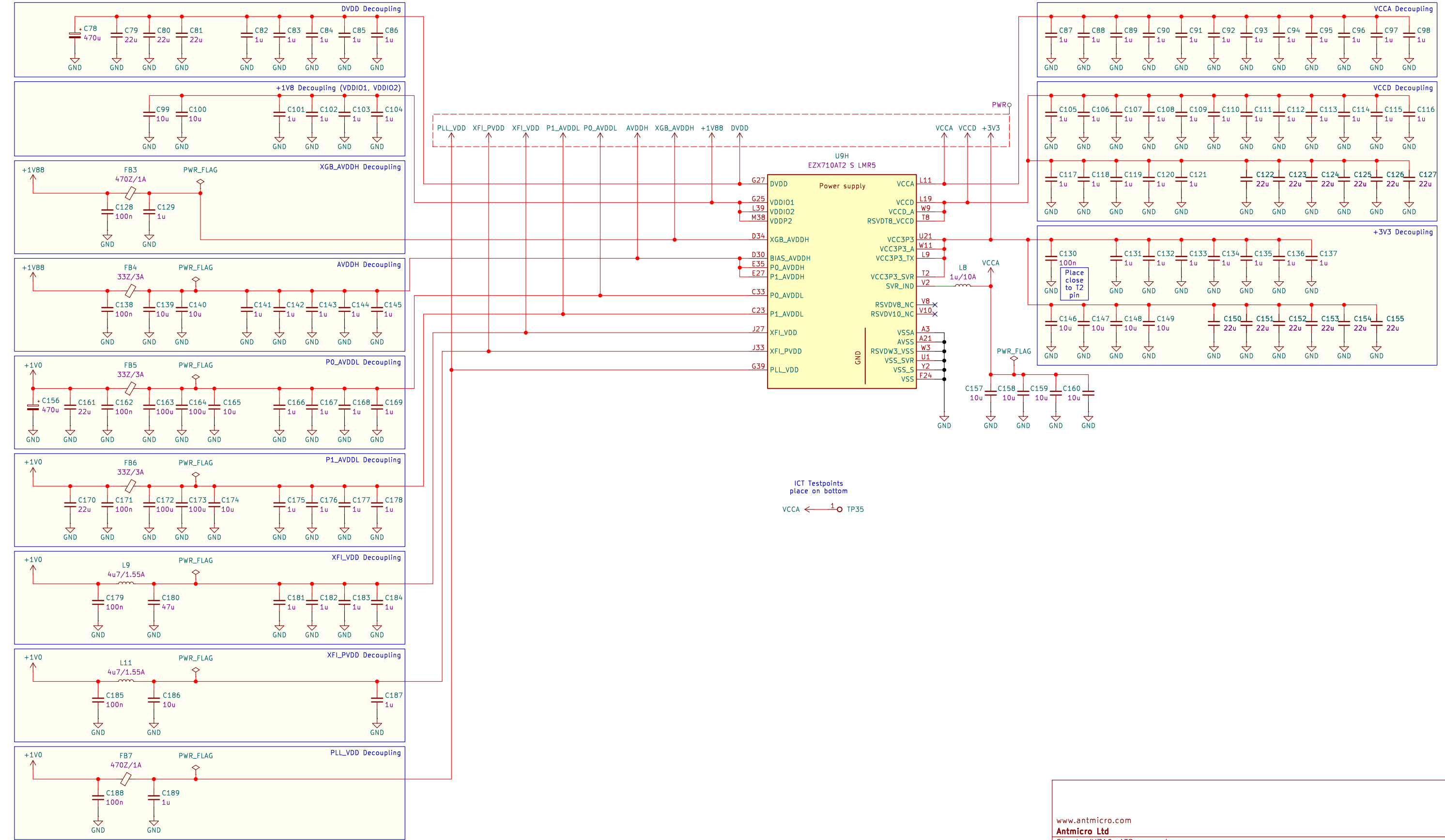


Flash Memory

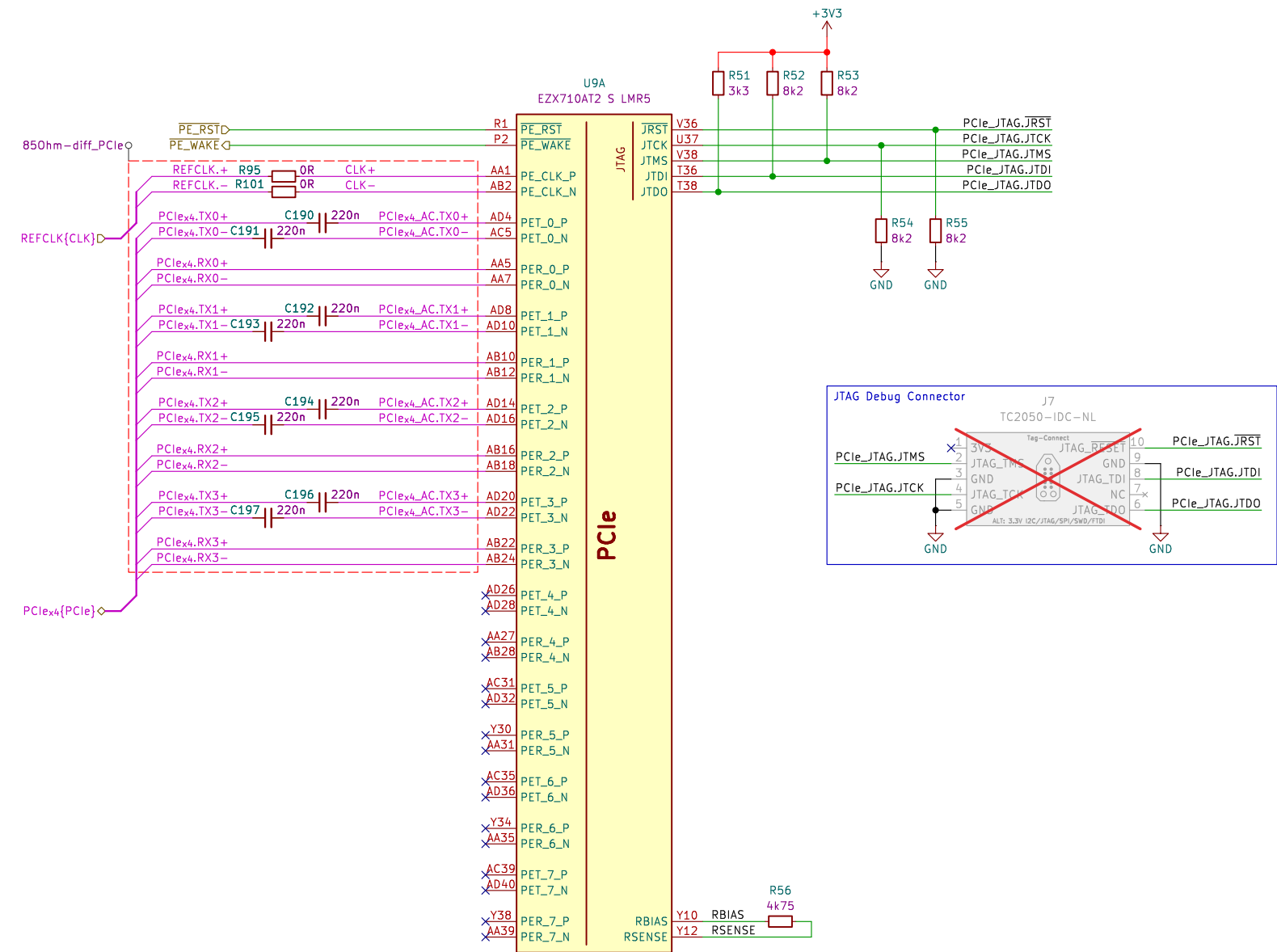




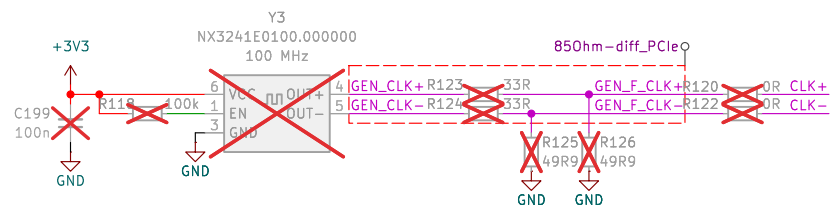
X710-AT2 Power



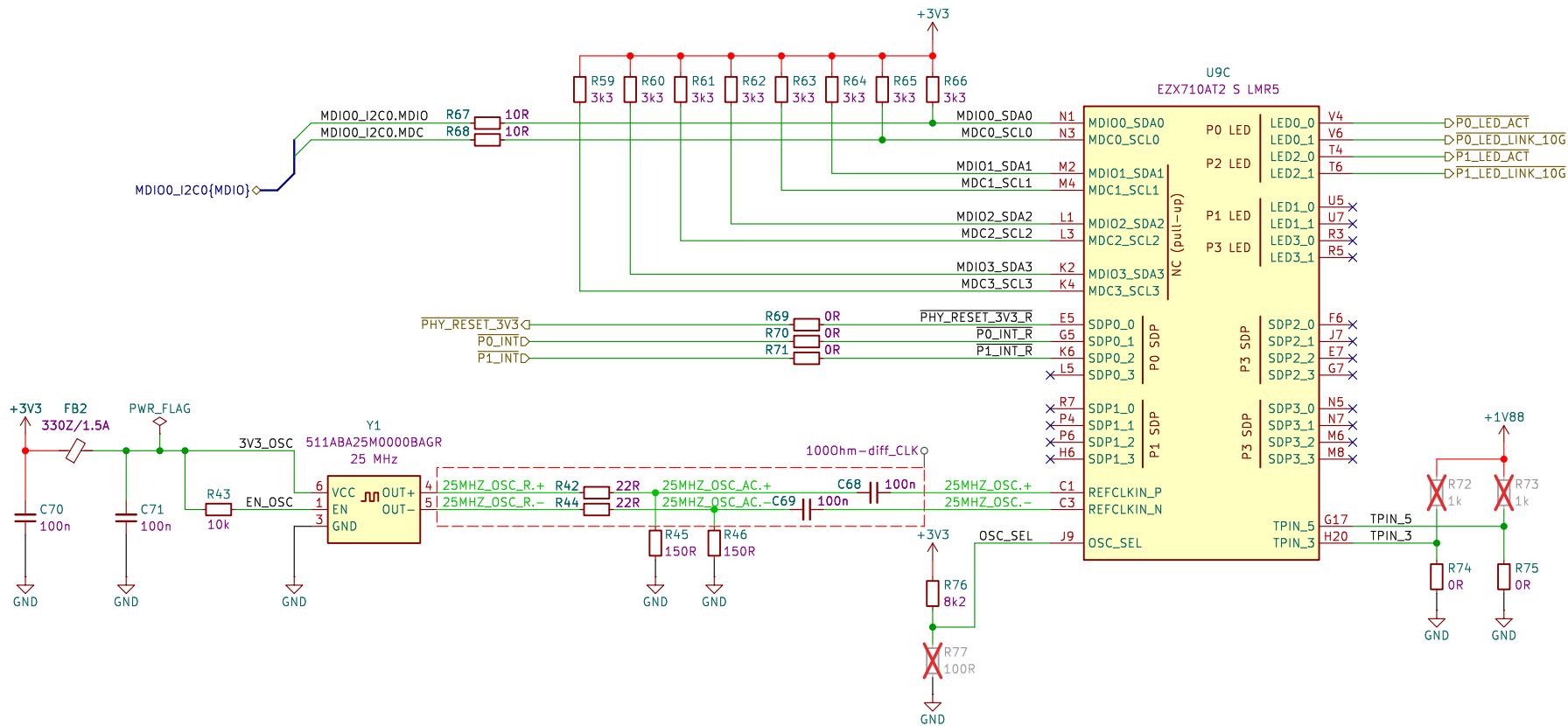
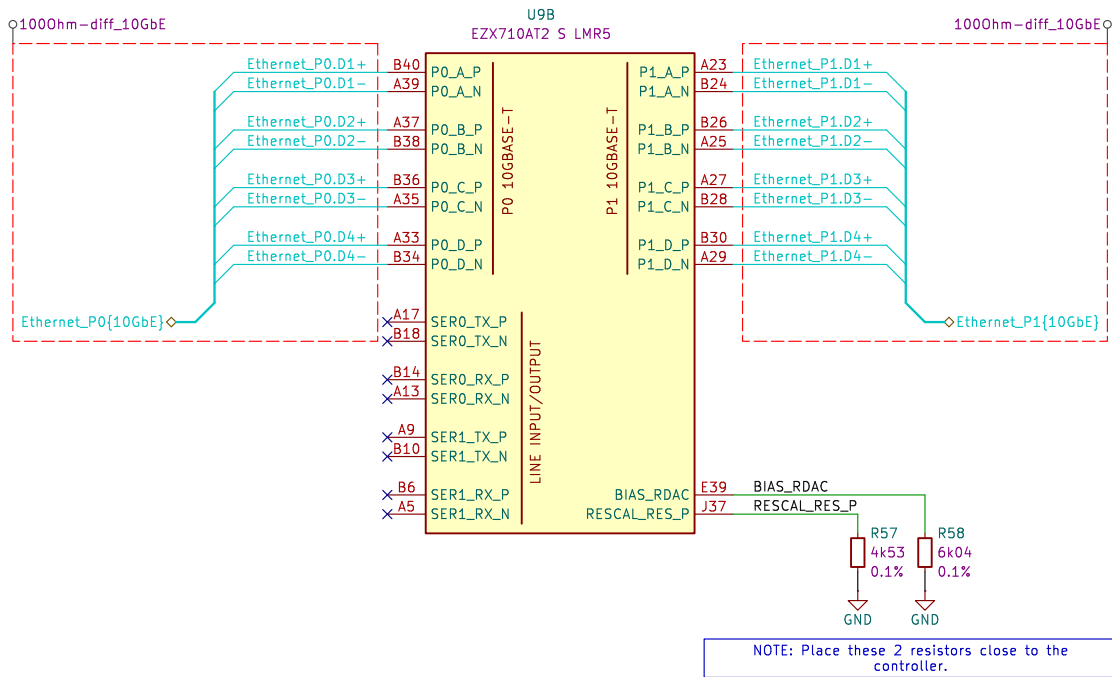
X710-AT2 PCIe Interface



NOTE: Place this resistor close to the controller;
the trace length should be less than 25.4 mm.

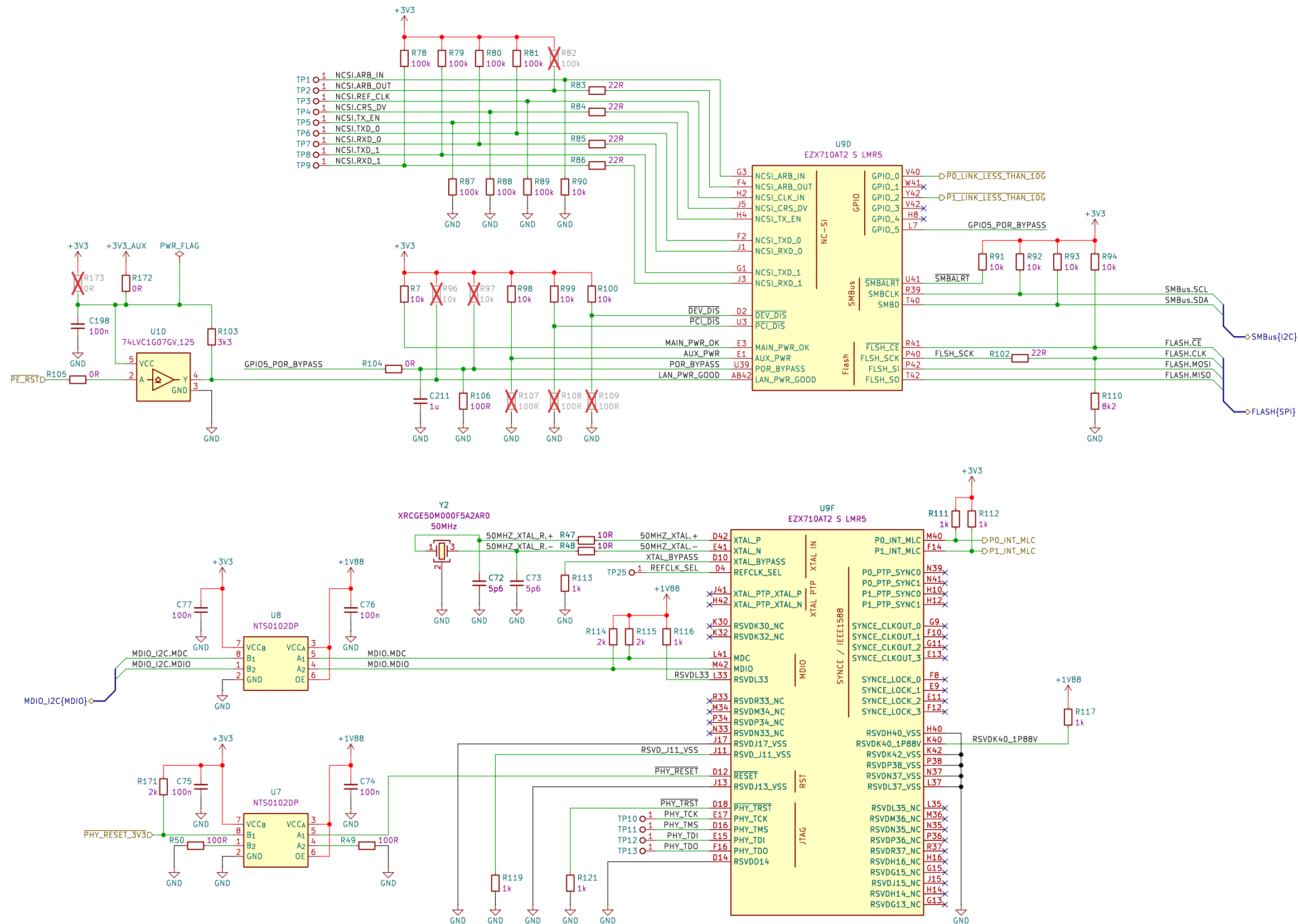


X710-AT2 10GbE Interface

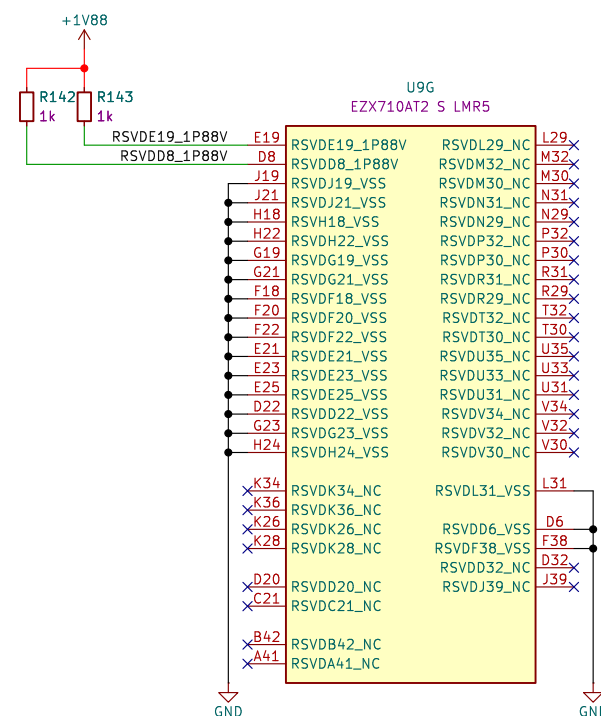
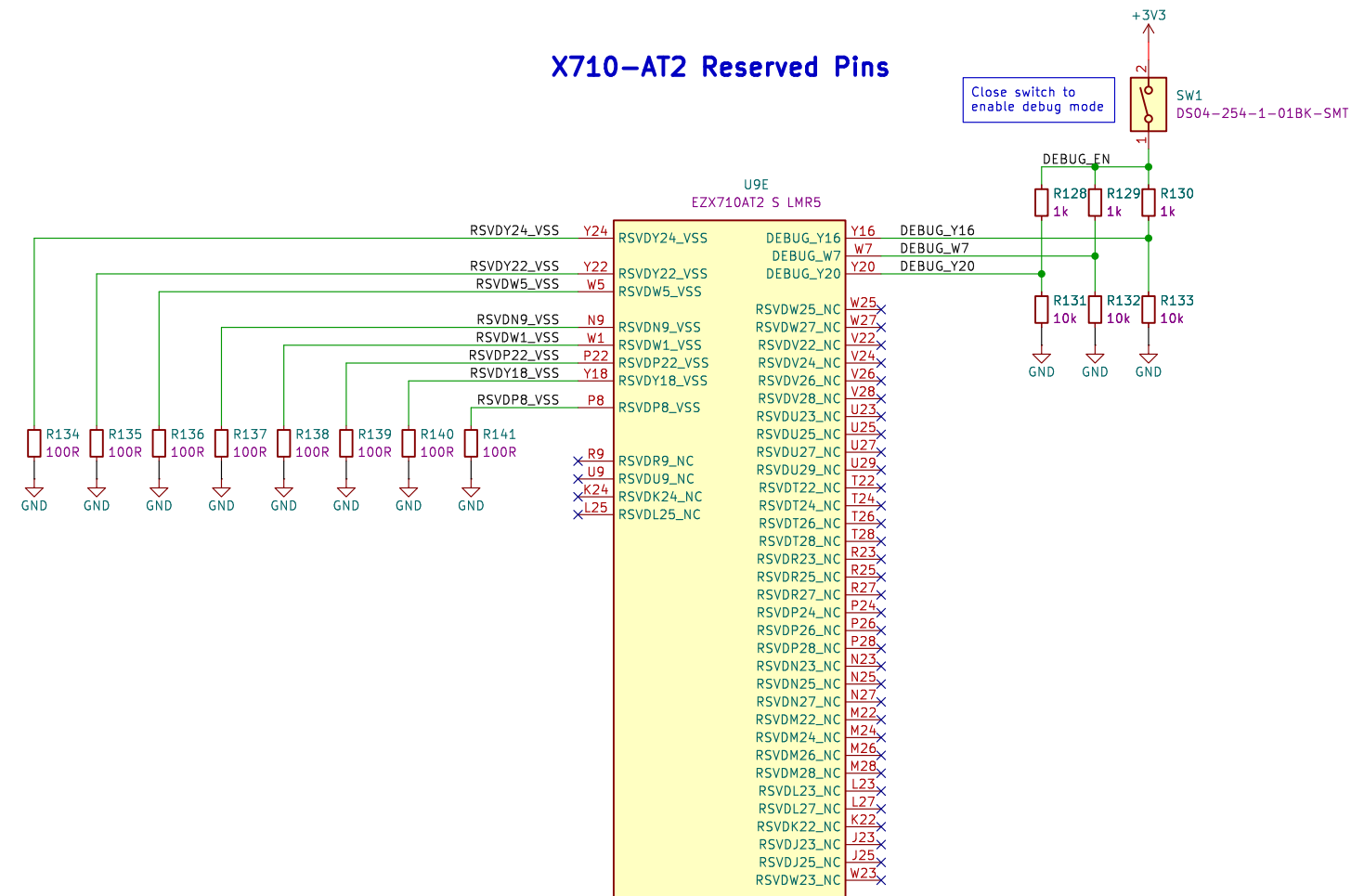


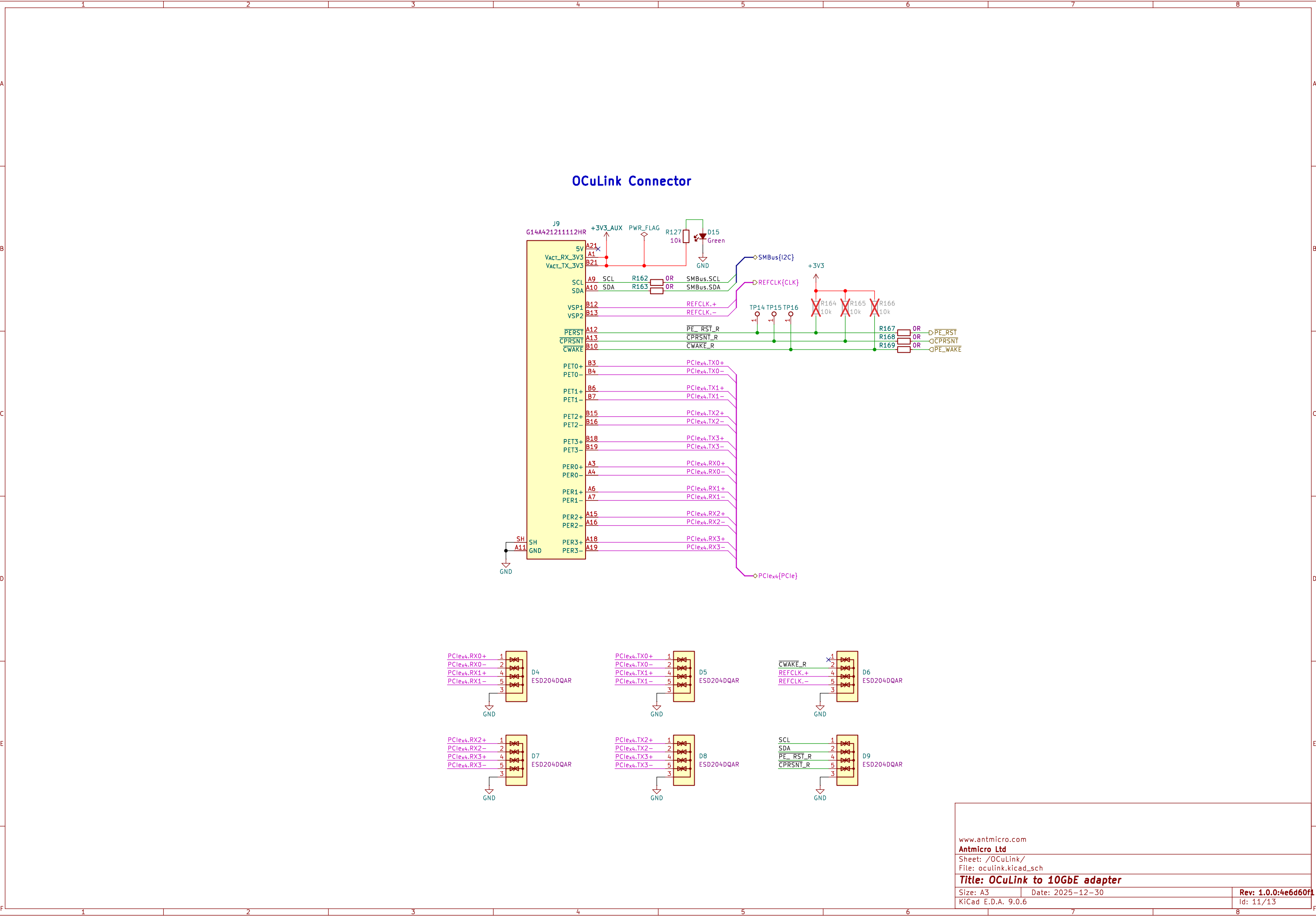
BASE-T PHY MDI Pair Swap Configuration	
Port 0	
TPIN_5 = 0 (Pull down to GND with 0R)	MDI pair swap
TPIN_5 = 1 (Pull up to 1V88 with 1k)	No MDI pair swap
Port 1	
TPIN_3 = 0 (Pull down to GND with 0R)	No MDI pair swap
TPIN_3 = 1 (Pull up to 1V88 with 1k)	MDI pair swap

X710-AT2 Miscellaneous



X710-AT2 Reserved Pins





Fan Controller

