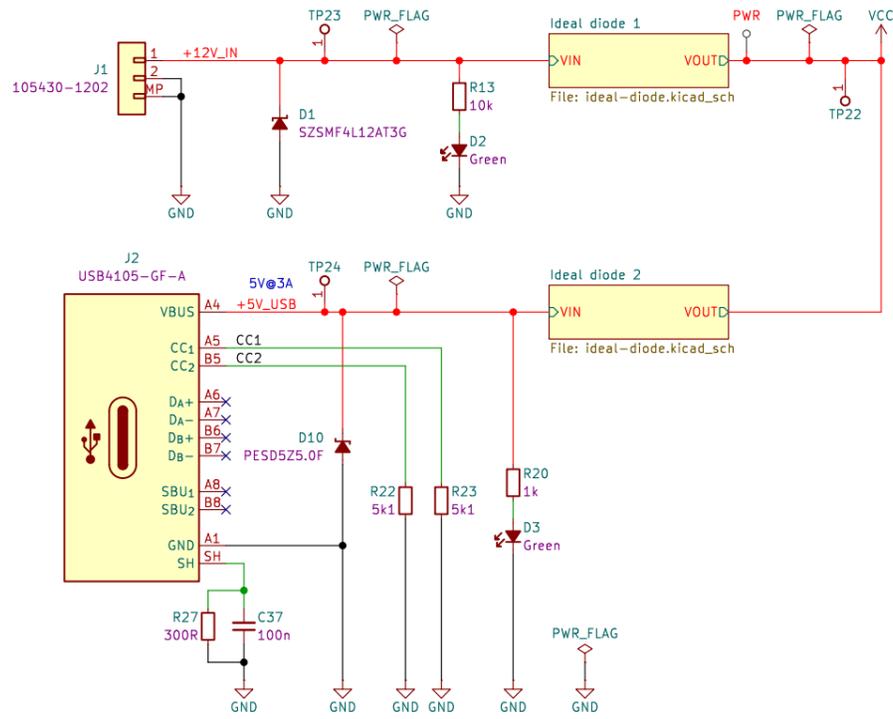


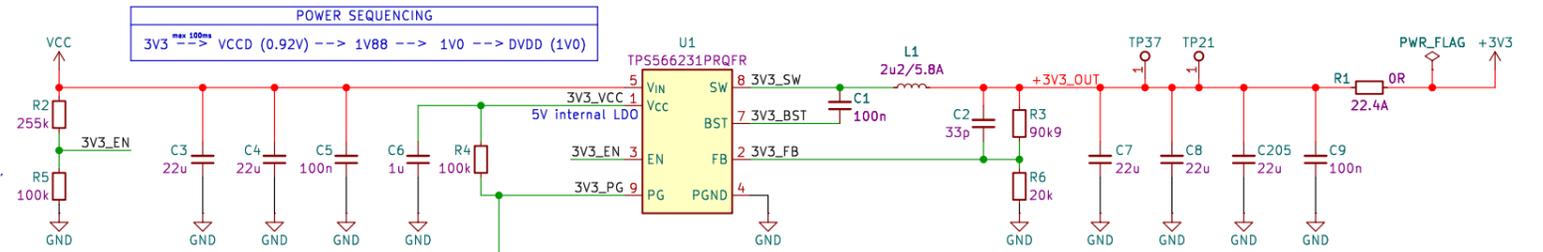
## Power Inputs



### 3V3 DC-DC

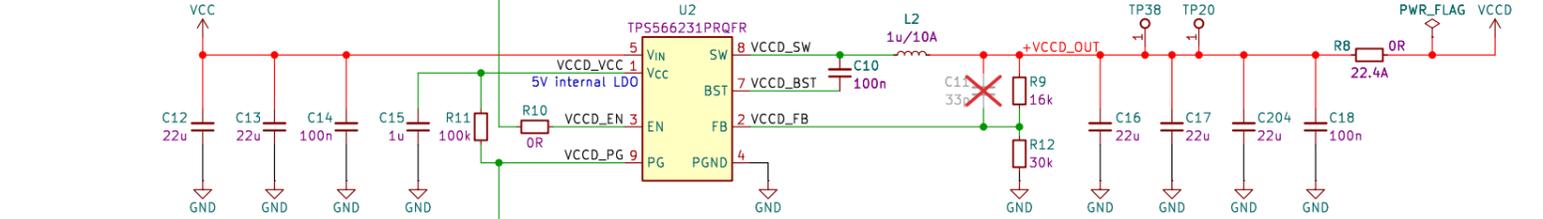
Requirements
3.14-3.47V Max ripple: 70mV <sub>p-p</sub>

UVLO  
3V3 rail is first in the power sequence, so deasserting its enable pin turns off remaining DC-DC converters



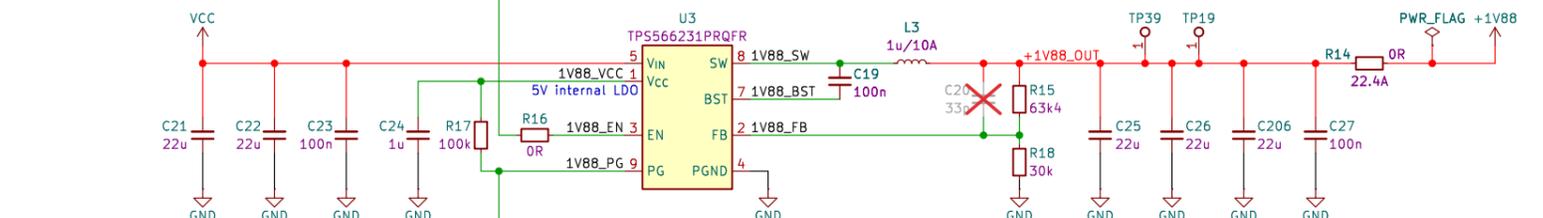
### 0V92 DC-DC

Requirements
0.87-0.96V Max ripple: 20mV <sub>p-p</sub>



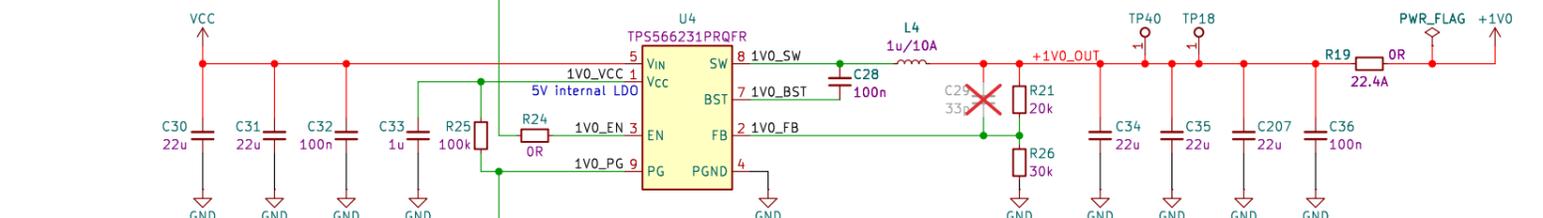
### 1V88 DC-DC

Requirements
1.8236-1.9364V Max ripple: 50mV <sub>p-p</sub>



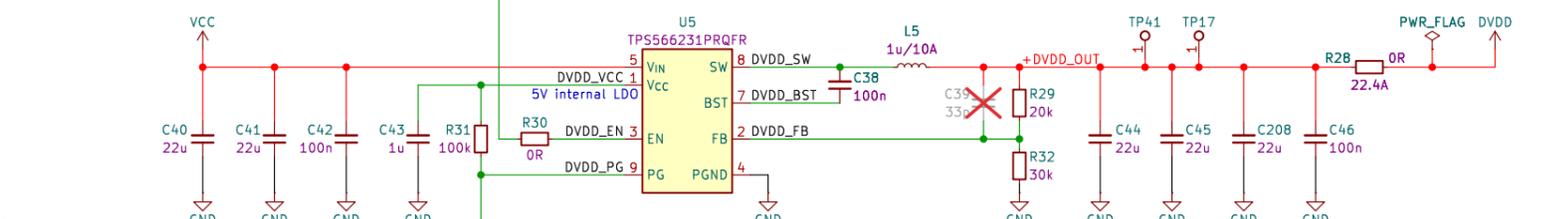
### 1V DC-DC

Requirements
0.95-1.05V Max ripple: 10mV <sub>p-p</sub>

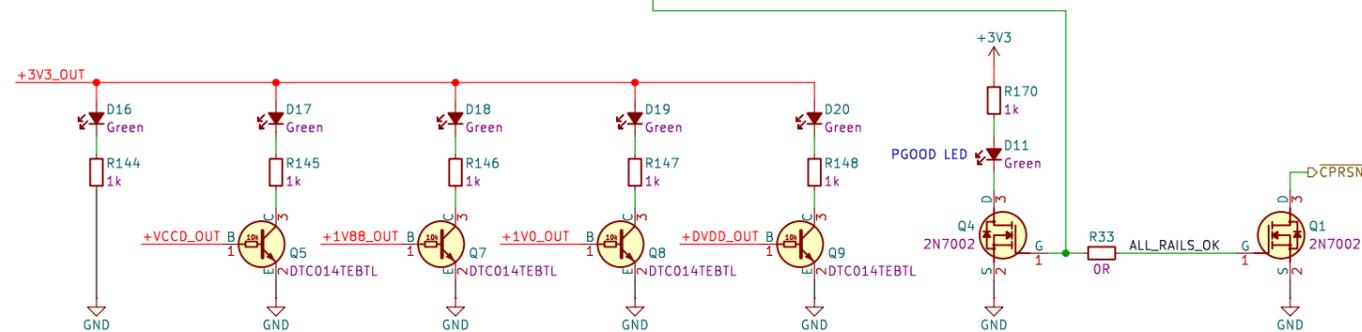


### DVDD DC-DC

Requirements
0.95-1.05V Max ripple: 30mV <sub>p-p</sub>



$$V_{out} = 0.6 \times (1 + R_H/R_L) [V]$$



### ICT Testpoints

[place on bottom]

- +12V\_IN 1 TP27
- +5V\_USB 1 TP28
- VCC 1 TP29
- +3V3 1 TP30
- VCCD 1 TP31
- +1V88 1 TP32
- +1V0 1 TP33
- DVDD 1 TP34

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Sheet: /Power/

File: power.kicad\_sch

Title: OCuLink to 10GbE adapter

Size: A3

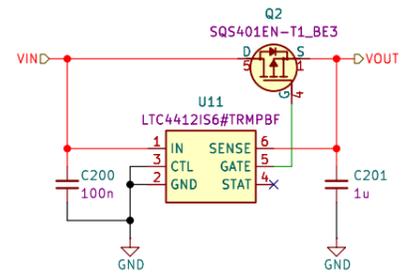
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Rev: 1.1.0:abfcdad

KiCad E.D.A. 9.0.7

Id: 2/13

### Ideal Diode



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Sheet: /Power/Ideal diode 1/

File: ideal-diode.kicad\_sch

**Title: OCuLink to 10GbE adapter**

Size: A3

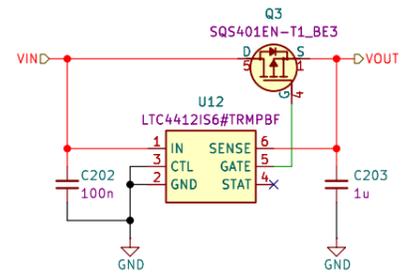
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Rev: 1.1.0:abfcdad

KiCad E.D.A. 9.0.7

Id: 12/13

### Ideal Diode



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**Title: OCuLink to 10GbE adapter**

Size: A3

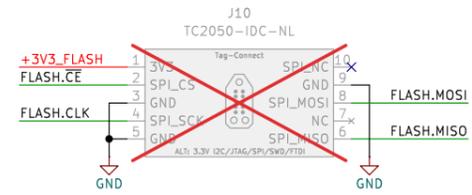
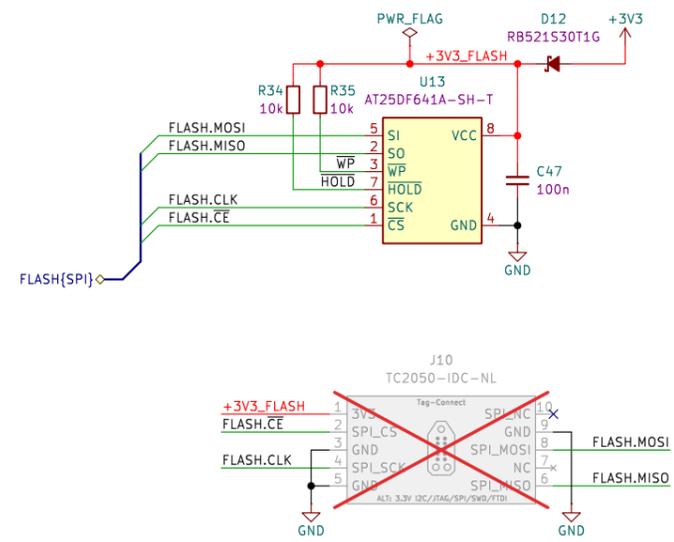
Date: 2026-02-23

Rev: 1.1.0:abfcdad

KiCad E.D.A. 9.0.7

Id: 13/13

## Flash Memory



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File: flash-memory.kicad\_sch

**Title: OCuLink to 10GbE adapter**

Size: A3

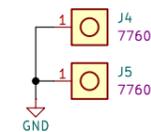
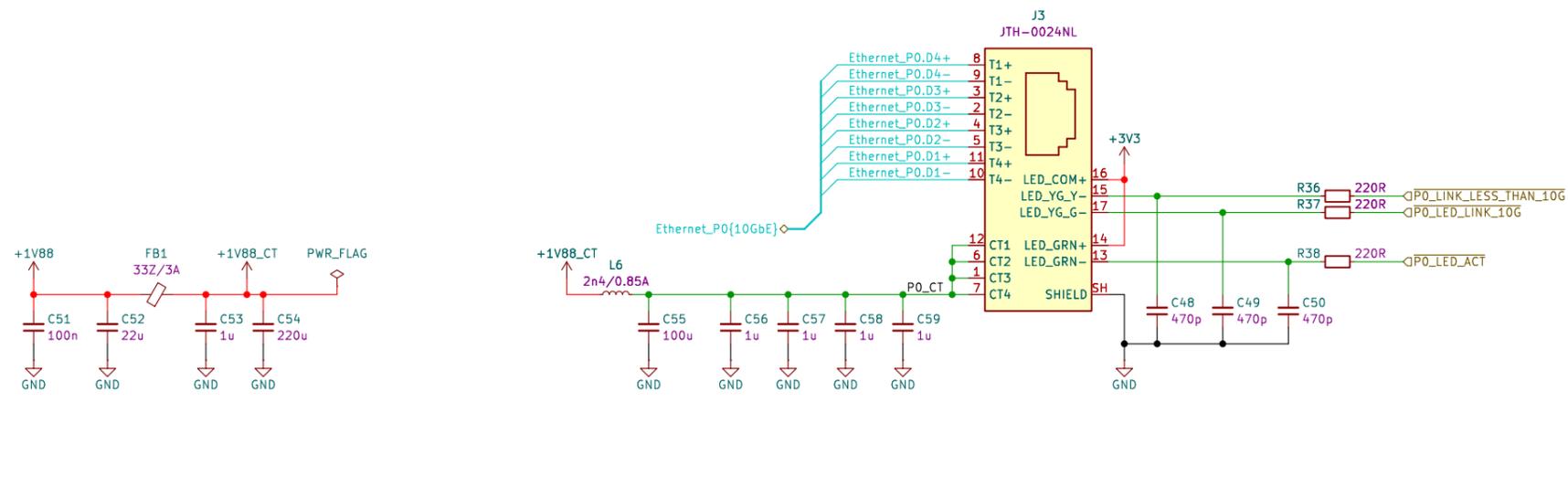
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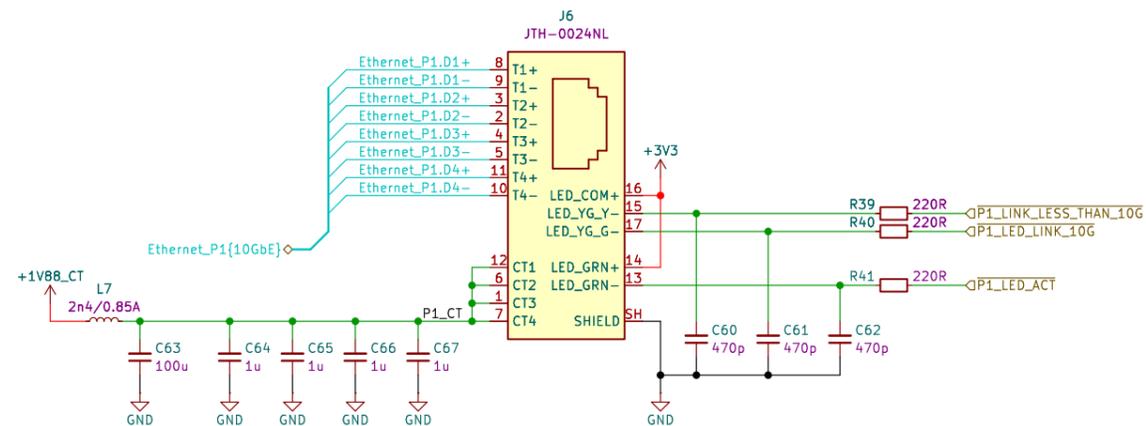
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Id: 3/13

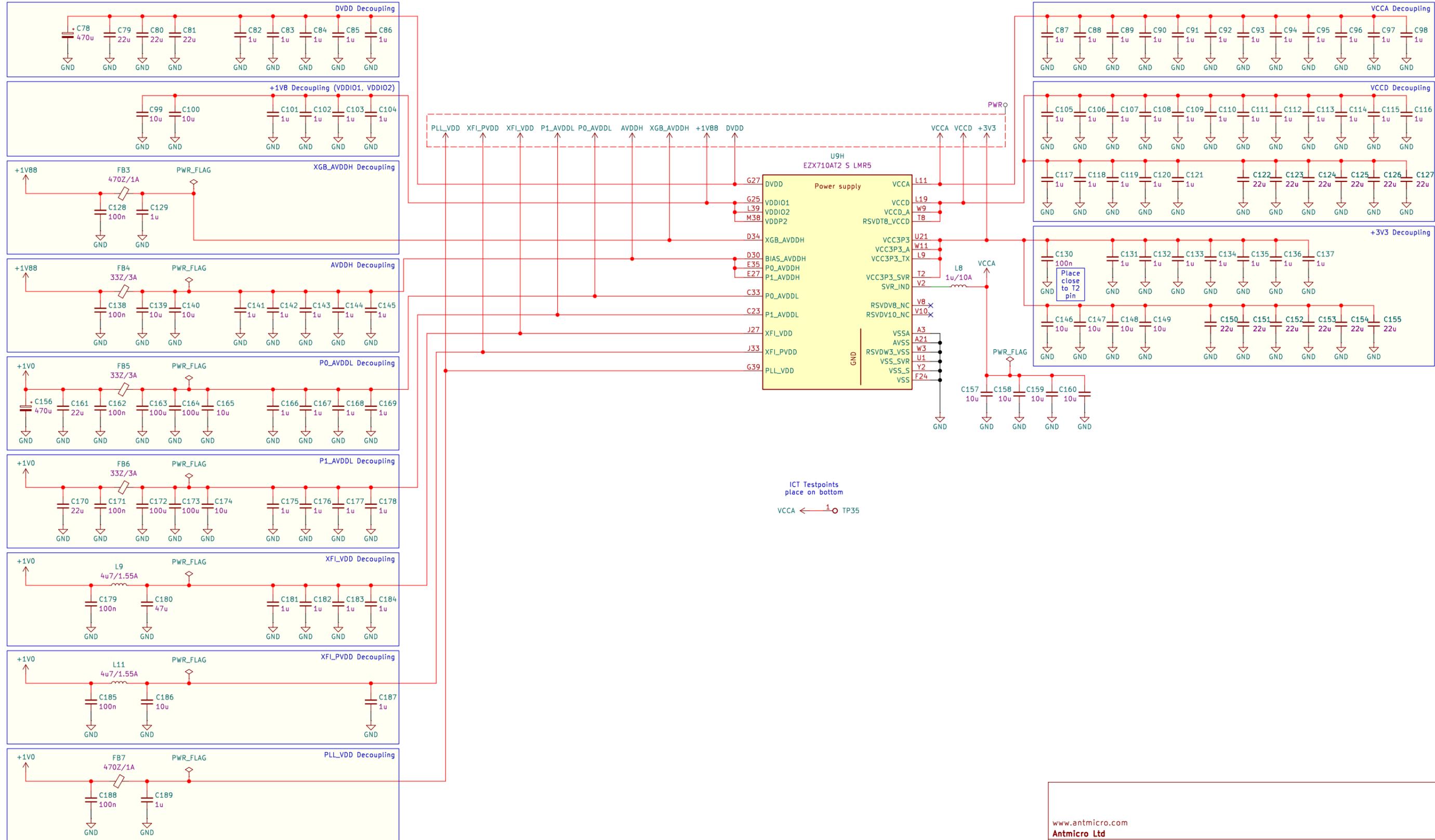
### 10GbE Port 0



### 10GbE Port 1

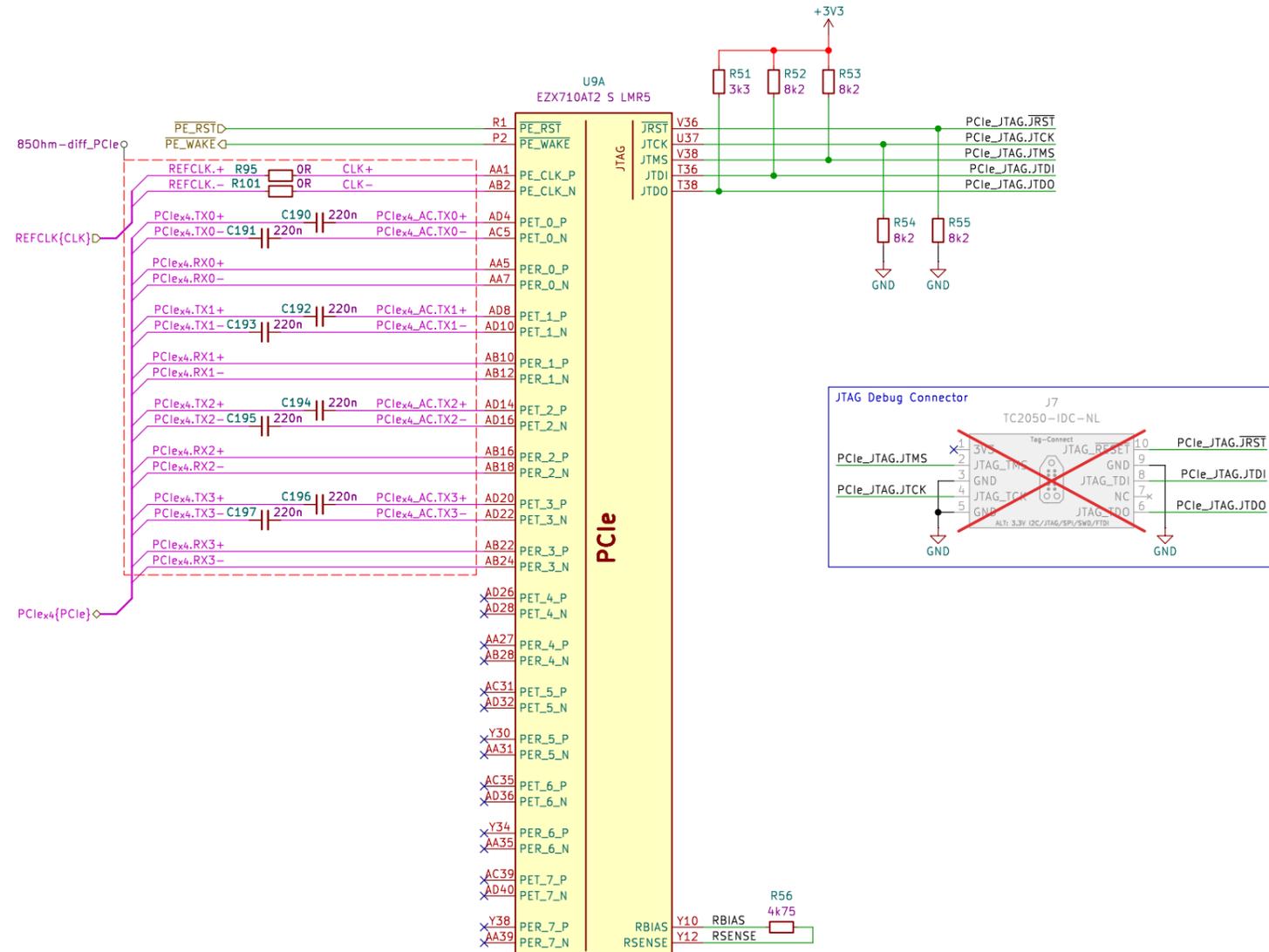


# X710-AT2 Power

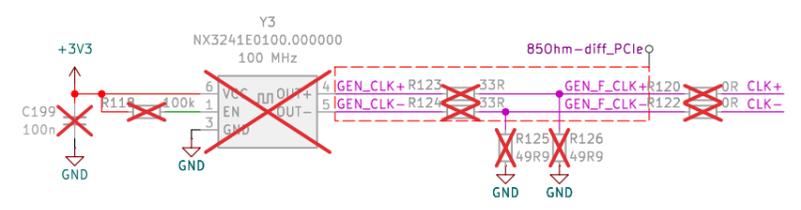


ICT Testpoints  
place on bottom  
VCCA ← TP35

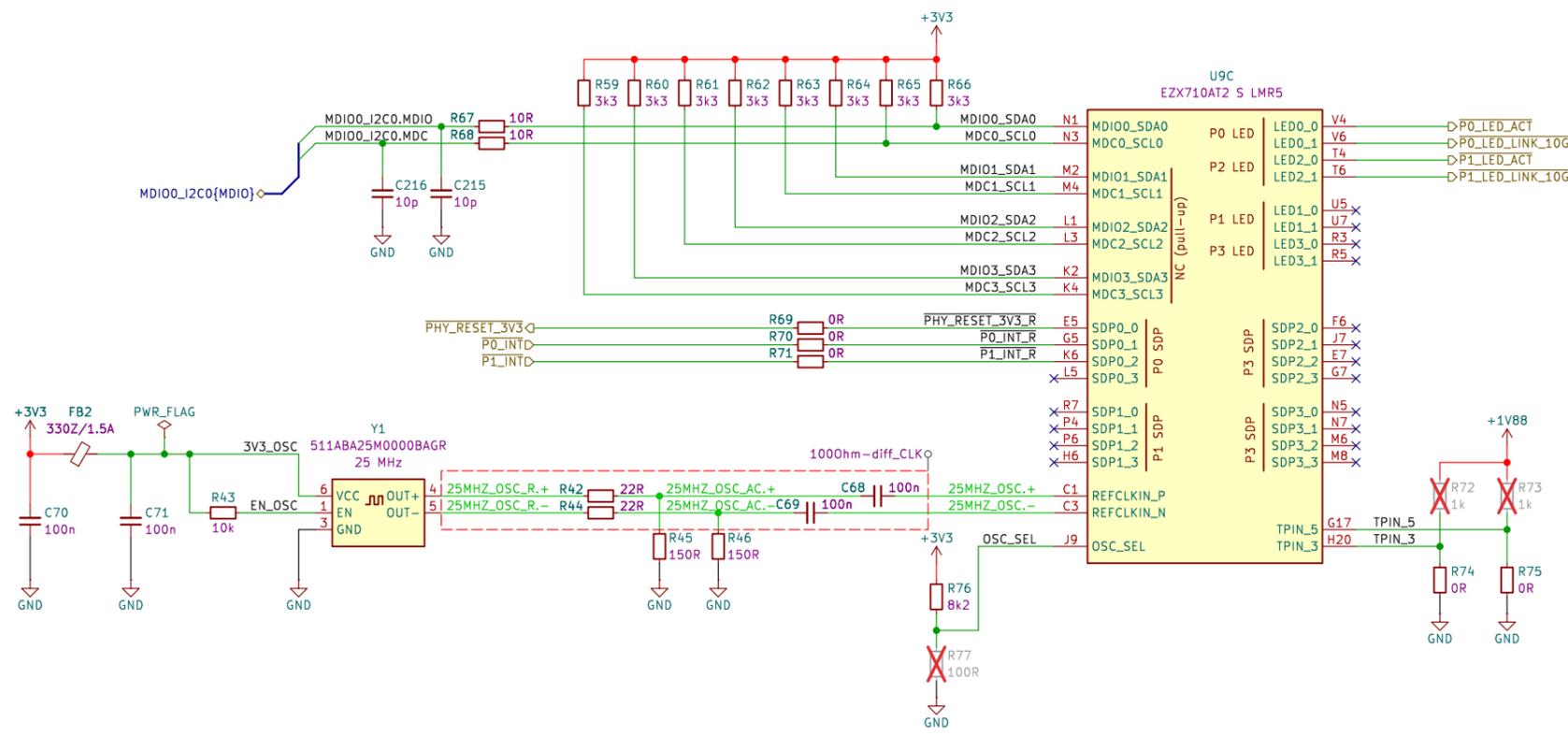
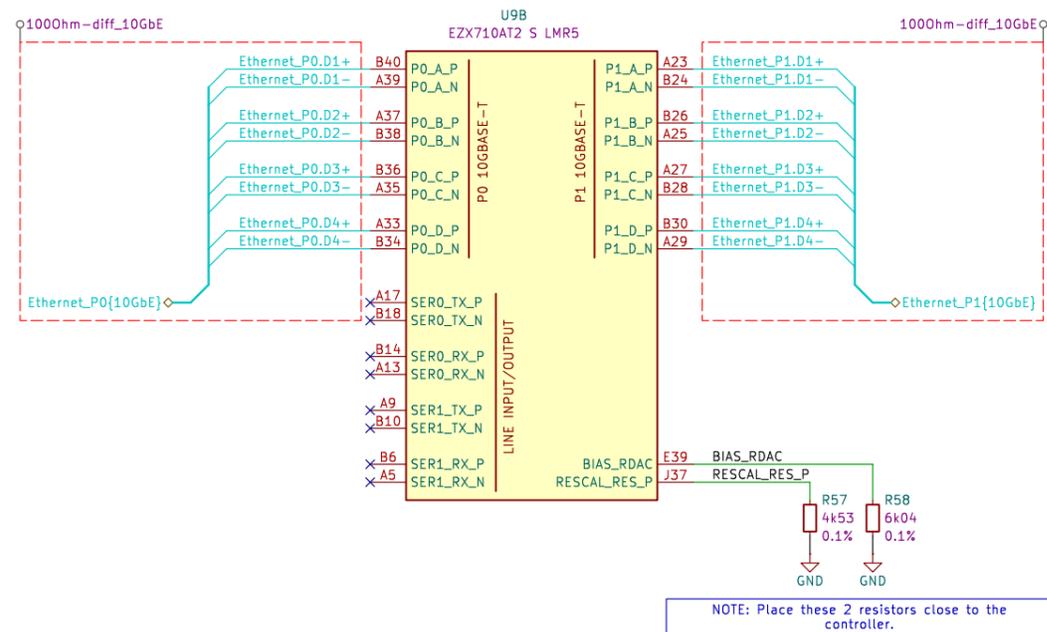
## X710-AT2 PCIe Interface



NOTE: Place this resistor close to the controller; the trace length should be less than 25.4 mm.



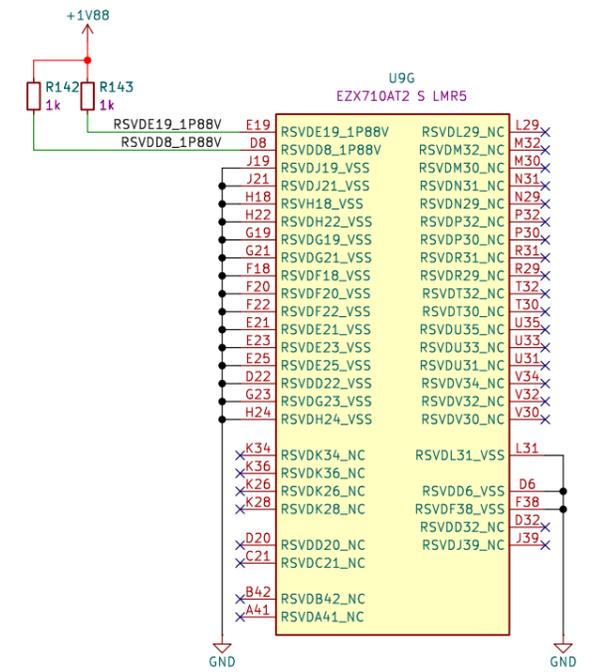
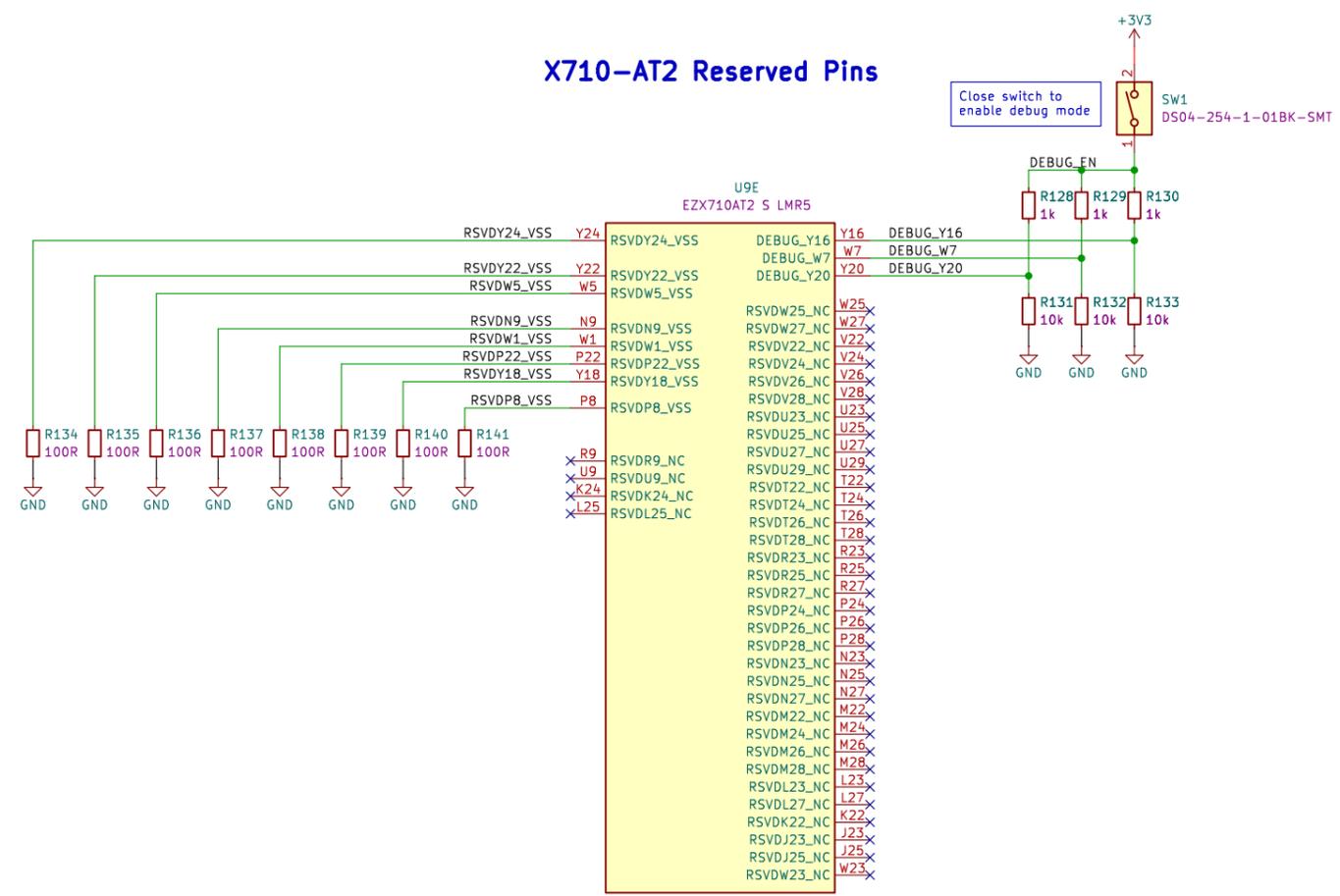
# X710-AT2 10GbE Interface



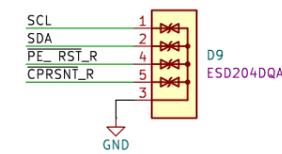
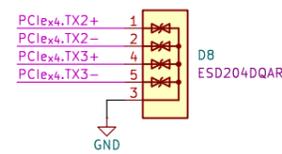
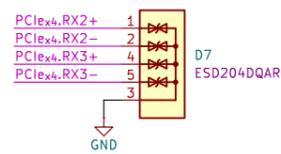
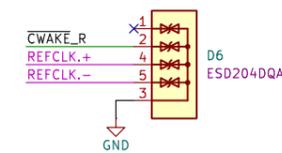
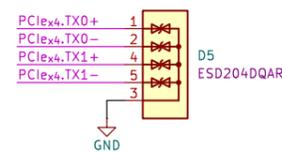
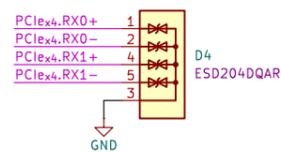
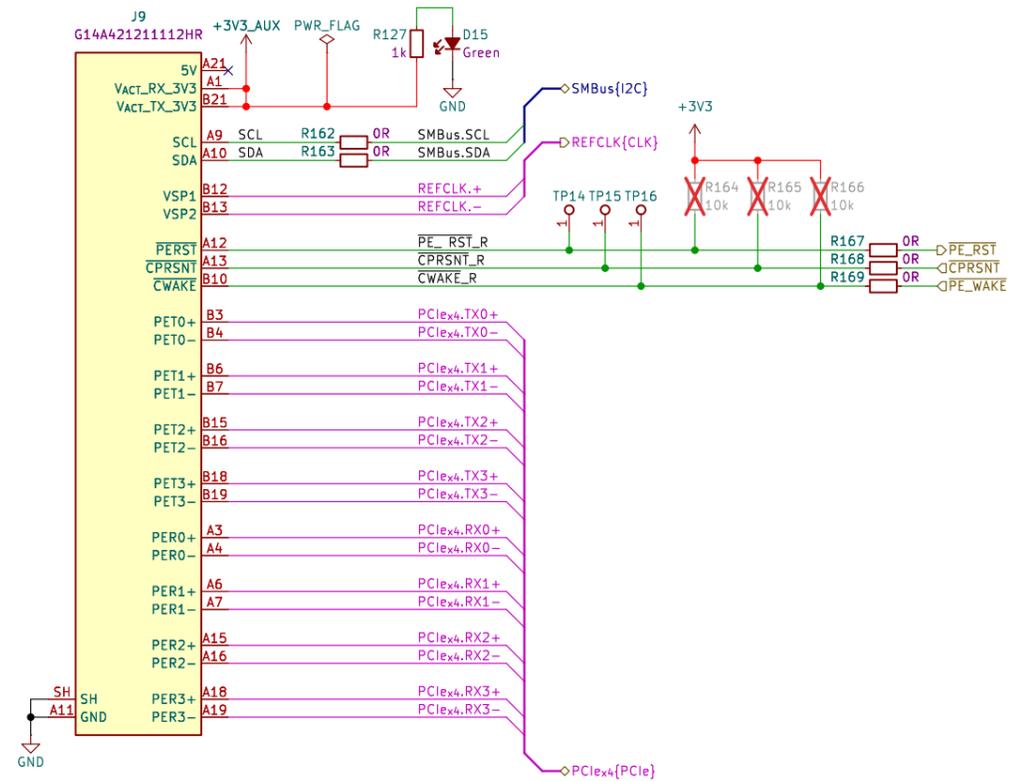
BASE-T PHY MDI Pair Swap Configuration	
Port 0	
TPIN_5 = 0 (Pull down to GND with 0R)	MDI pair swap
TPIN_5 = 1 (Pull up to 1V88 with 1k)	No MDI pair swap
Port 1	
TPIN_3 = 0 (Pull down to GND with 0R)	No MDI pair swap
TPIN_3 = 1 (Pull up to 1V88 with 1k)	MDI pair swap



### X710-AT2 Reserved Pins



## OCuLink Connector



## Fan Controller

